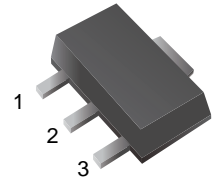


## Transistor (PNP)

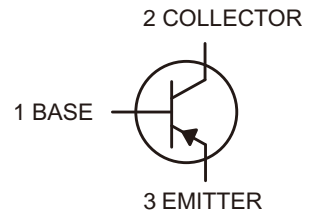
### Features

- For Switching and AF Amplifier Applications
- Silicon Epitaxial Chip
- Lead free in comply with EU RoHS 2011/65/EU directives



### Ordering Information

Part Number	Marking	Shipping	Reel
LXT5401-TR1	B2L	1000PCS Tape&Reel	7 inchs
LXT5401-TR3	B2L	3000PCS Tape&Reel	13 inchs



### Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Collector Base Voltage	$-V_{CBO}$	160	V
Collector Emitter Voltage	$-V_{CEO}$	150	V
Emitter Base Voltage	$-V_{EBO}$	5	V
Collector Current	$-I_C$	600	mA
Maximum Power Dissipation	$P_D$	625	mW
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C



Electrical Characteristics (T<sub>A</sub>=25°C)

Parameter	Symbol	Min	Max	Unit
DC Current Gain at V <sub>CE</sub> = -5 V, I <sub>C</sub> = -1 mA at V <sub>CE</sub> = -5 V, I <sub>C</sub> = -10 mA at V <sub>CE</sub> = -5 V, I <sub>C</sub> = -50 mA	H <sub>FE</sub>	50 60 50	240	
Collector Base Cutoff Current at V <sub>CB</sub> = -120 V	-I <sub>CBO</sub>		50	nA
Emitter Base Cutoff Current at V <sub>EB</sub> = -3 V	-I <sub>EBO</sub>		50	nA
Collector Base Breakdown Voltage at I <sub>C</sub> = -100 μA	-V <sub>(BR)CBO</sub>	160		V
Collector Emitter Breakdown Voltage at I <sub>C</sub> = -1 mA	-V <sub>(BR)CEO</sub>	150		V
Emitter Base Breakdown Voltage at I <sub>E</sub> = -10 μA	-V <sub>(BR)EBO</sub>	5		V
Collector Emitter Saturation Voltage at I <sub>C</sub> = -10 mA, I <sub>B</sub> = -1 mA at I <sub>C</sub> = -50 mA, I <sub>B</sub> = -5 mA	-V <sub>CE(sat)</sub>		0.2 0.5	V
Base Emitter Saturation Voltage at I <sub>C</sub> = -10 mA, I <sub>B</sub> = -1 mA at I <sub>C</sub> = -50 mA, I <sub>B</sub> = -5 mA	-V <sub>BE(sat)</sub>		1 1	V
Transition Frequency at V <sub>CE</sub> = -10 V, I <sub>C</sub> = -10 mA, f = 100 MHz	F <sub>T</sub>	100	300	MHz
Output Capacitance at V <sub>CB</sub> = -10 V, f = 1 MHz	C <sub>ob</sub>		6	pF



Characteristics Curves

Fig.1  $V_{CEsat}$  vs  $I_C$

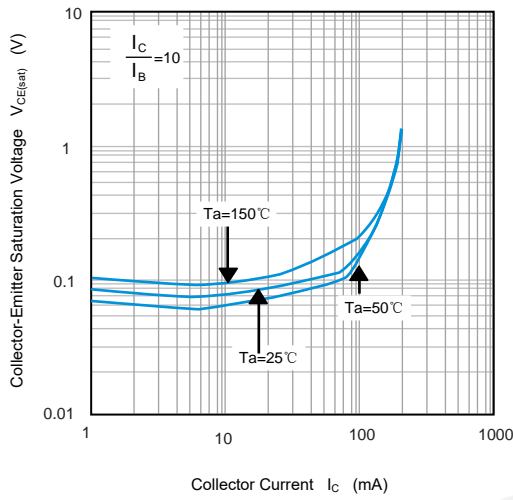


Fig.2  $h_{FE}$  vs  $I_C$

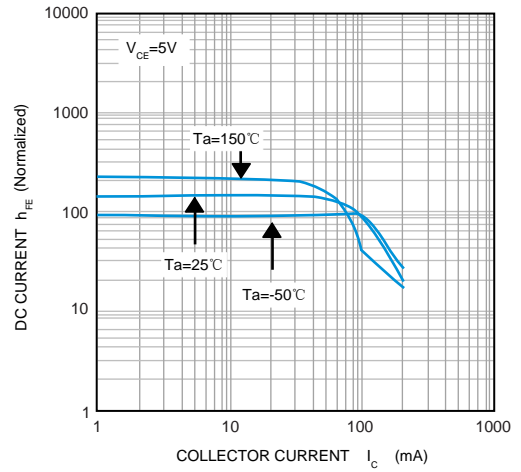


Fig.3  $V_{BE(on)}$  vs  $I_C$

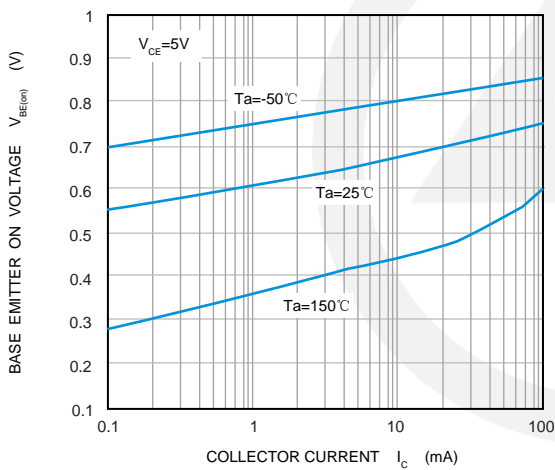


Fig.4  $f_T$  vs  $I_C$

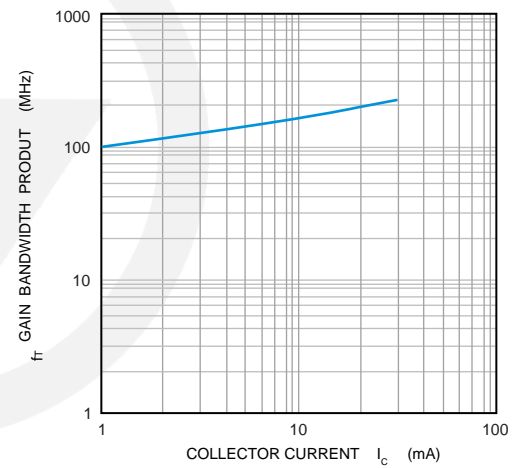
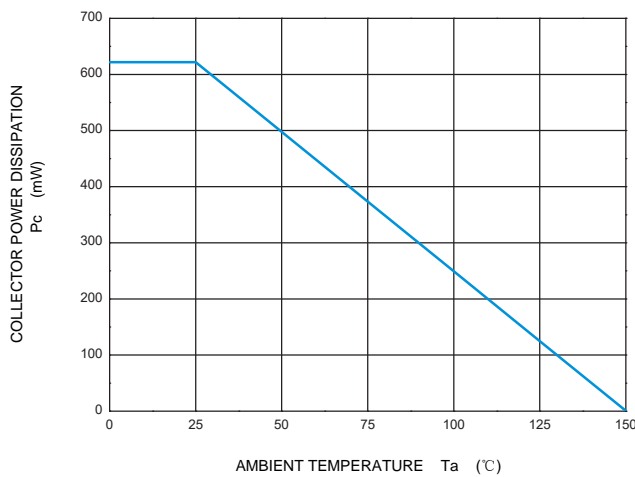
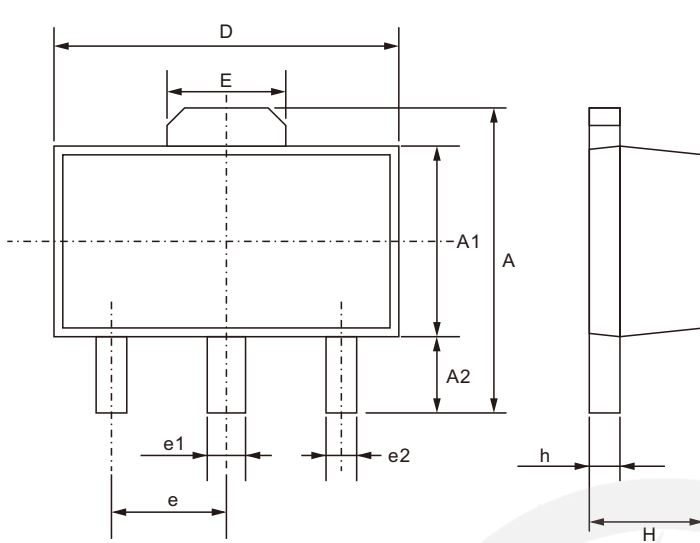


Fig.5  $P_C$  vs  $T_a$





### SOT-89 Package Outline

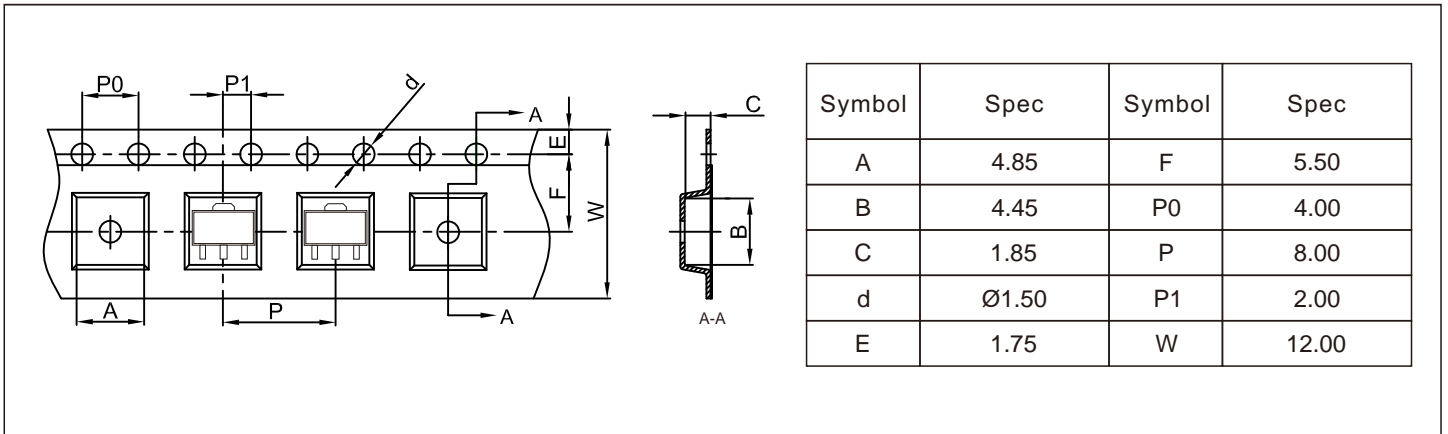


Unit: mm

SYMBOL	DIMENSIONS	
	MIN.	MAX.
A	3.750	4.250
A1	2.400	2.600
A2	0.950	1.050
D	4.400	4.600
E	1.500	1.600
e1	0.470	0.530
e2	0.350	0.450
e	1.500 TYP.	
H	1.400	1.600
h	0.300	0.500

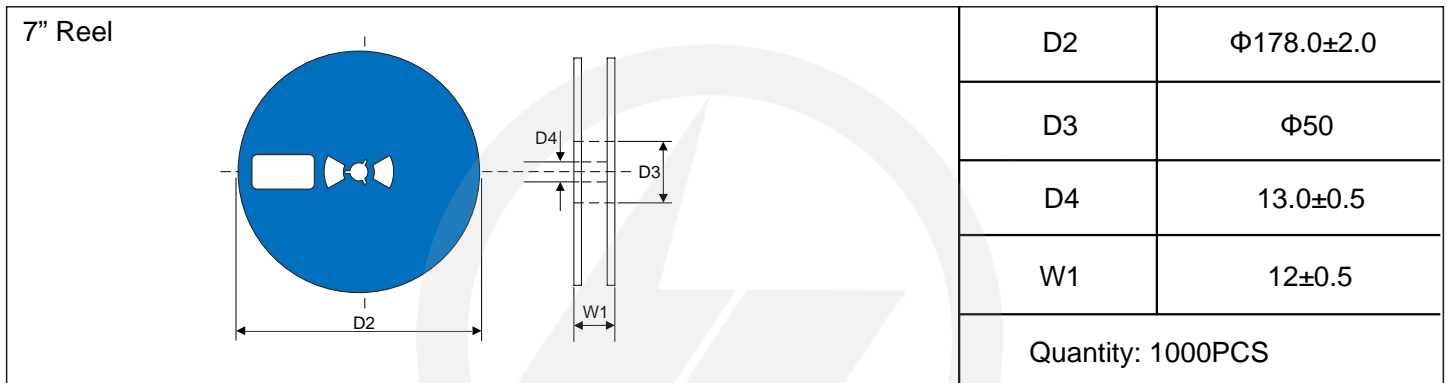
**Carrier Tape Dimensions**

Unit : mm



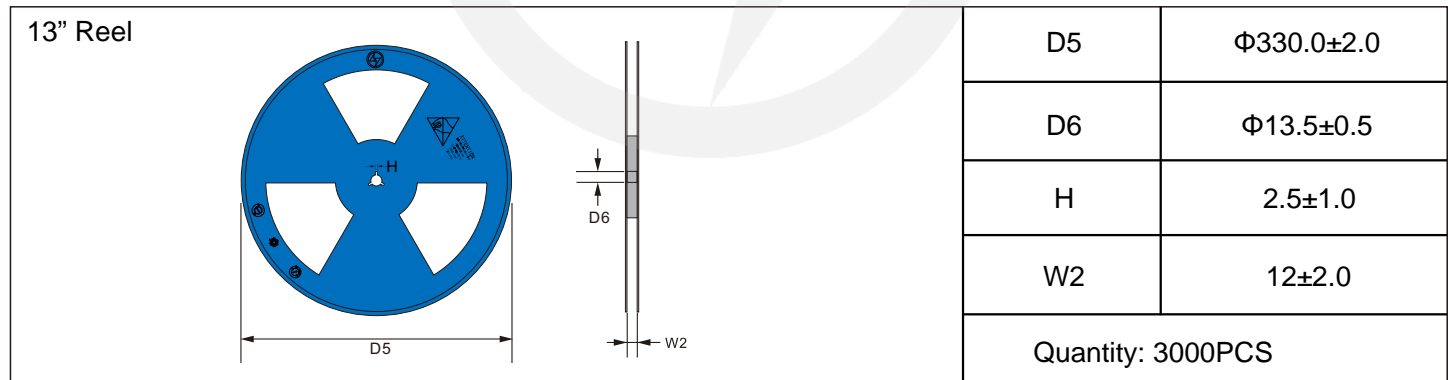
**Reel Dimensions**

Unit : mm

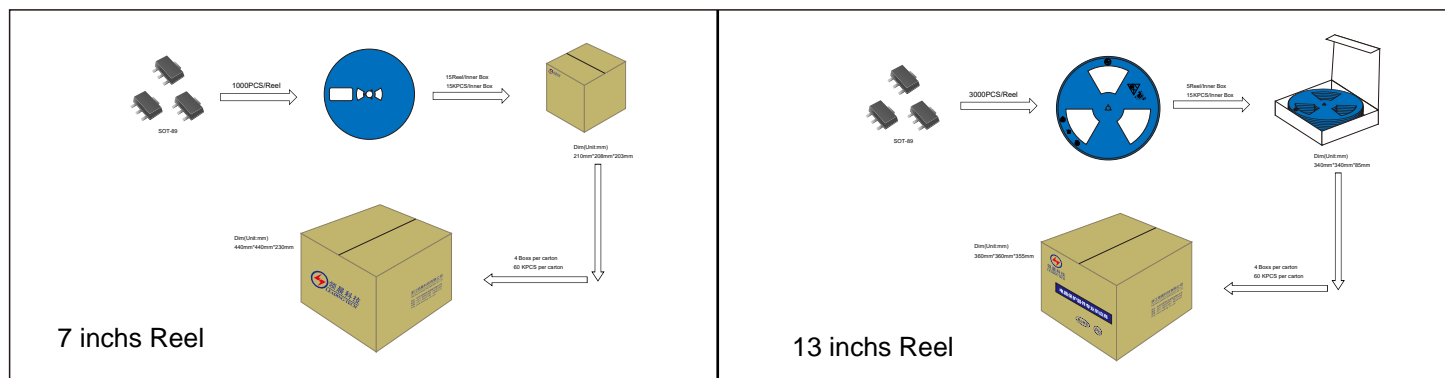


**Reel Dimensions**

Unit : mm

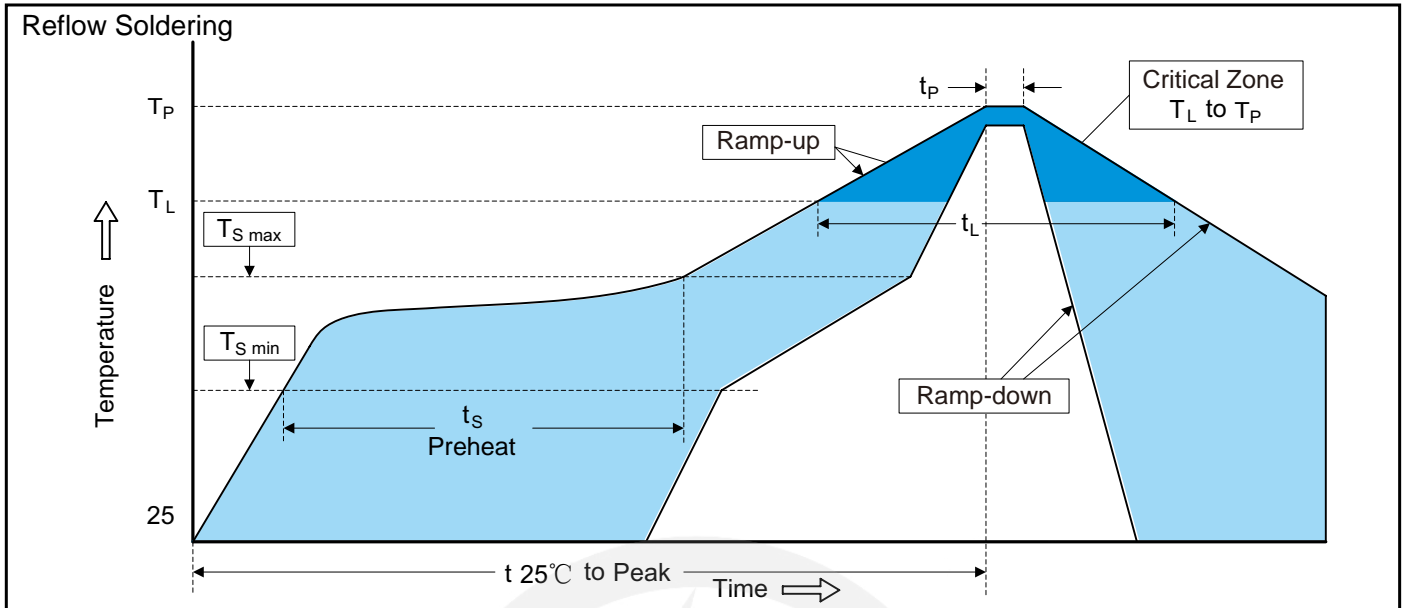


**Packaging**





## Recommended Soldering Conditions



### Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate ( $T_L$ to $T_P$ )	3°C/second max.
Preheat	
-Temperature Min ( $T_{S\ min}$ )	150°C
-Temperature Max ( $T_{S\ max}$ )	200°C
-Time (min to max) ( $t_s$ )	60-180 seconds
$T_{S\ max}$ to $T_L$	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature ( $T_L$ )	217°C
-Time ( $t_L$ )	60-150 seconds
Peak Temperature ( $T_P$ )	260°C
Time within 5°C of actual Peak Temperature ( $t_p$ )	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

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## Version Update Information

Series NO.	Enactment/Revision Date	Effective Date	Version	Revision content	Revision Reason	Revision Person	Note
01	2024.10.10	2024.10.10	3.0	New File	/	Ding	
02	2025.06.11	2025.06.11	3.1	Update packaging information	/	Ding	