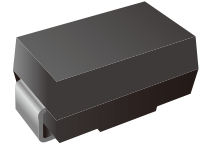


Thyristor Surge Suppressors (TSS) Data Sheet

Features

- Excellent capability of absorbing transient surge
- Quick response to surge voltage(ns Level)
- Eliminates overvoltage caused by fast rising transients
- Moisture sensitivity level: level 1
- Non degenerative
- Lead free in comply with EU RoHS 2011/65/EU directives



Applications

- Video

Ordering Information

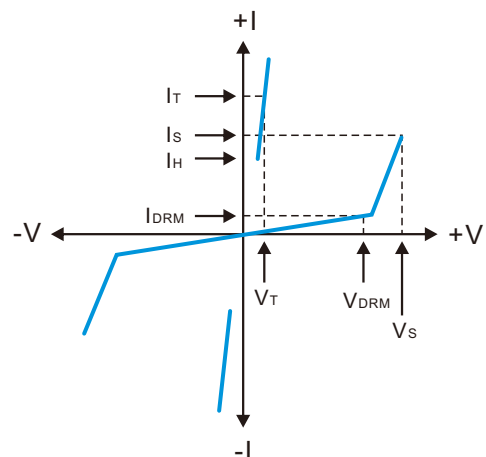
Part Number	Marking	Shipping	Reel
LTPA08AL-TR5	P08A XXXX	5000PCS Tape&Reel	13 inches
LTPA08AL-TR7K5	P08A XXXX	7500PCS Tape&Reel	13 inches

Thermal Considerations

Parameter	Symbol	Value	Unit
Operating Junction Temperature	T_J	-40 to +150	$^{\circ}\text{C}$
Storage Temperature Range	T_S	-40 to +150	$^{\circ}\text{C}$
Junction to Ambient on printed circuit	$R_{\theta JA}$	90	$^{\circ}\text{C/W}$

Electrical Parameters

Parameter	Definition
V_{DRM}	Peak Off-state Voltage – maximum voltage that can be applied while maintaining off state
V_S	Switching Voltage – maximum voltage prior to switching to on state
V_T	On-state Voltage – maximum voltage measured at rated on-state current
I_{DRM}	Leakage Current – maximum peak off-state current measured at V_{DRM}
I_S	Switching Current – maximum current required to switch to on state
I_T	On-state Current – maximum rated continuous on-state current
I_H	Holding Current – typical current required to maintain on state
C_O	Off-state Capacitance – typical capacitance measured in off state
I_{PP}	Peak Pulse Current – maximum rated peak impulse current
I_{TSM}	Peak One-cycle Surge Current – maximum rated one-cycle AC current
di/dt	Rate of Rise of Current – maximum rated value of the acceptable rate of rise in current over time



Electrical Characteristics

Part Number	V _{DRM} (V)	V _S (V)	V _T (V)	I _{DRM} (μA)	I _S (mA)	I _T (A)	I _H (mA)	C _O (pF) Max	Marking
LTPA08AL	6	13	4	5	800	2.2	10	20	P08A XXXX

Notes:

- All measurements are made at an ambient temperature of 25°C. I_{PP} applies to -40°C through +85°C temperature range.
- Off-state capacitance(C_O) is measured at 1 MHz with a 2V bias and is typical value.
- Rating Surge Voltage: 4KV, ±5 times (10/700μs)

Surge Ratings

I _{PP} 2×10μs (A)	I _{PP} 8×20μs (A)	I _{PP} 10×160μs (A)	I _{PP} 10×560μs (A)	I _{PP} 10×1000μs (A)	I _{TSM} 60Hz (A)	di/dt (A/μs)
150	150	90	50	45	20	500

Characteristics Curves

Fig.1 tr x td Pulse Wave-form

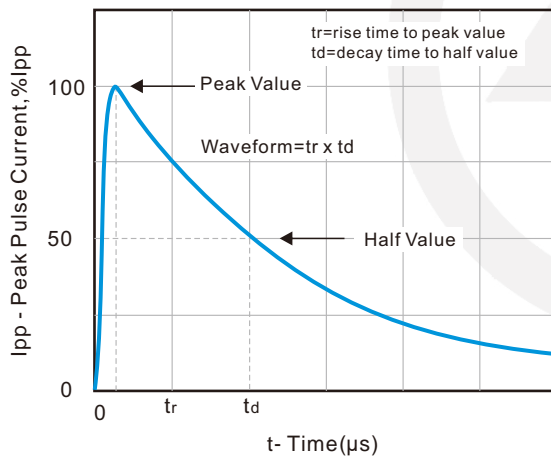


Fig.2 Normalized Vs Change versus Junction Temperature

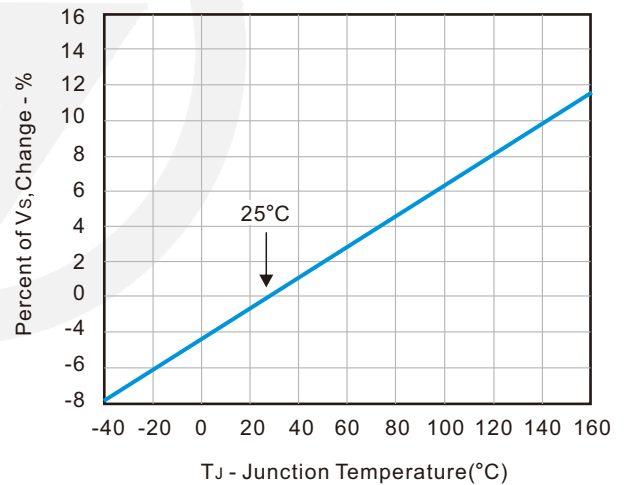
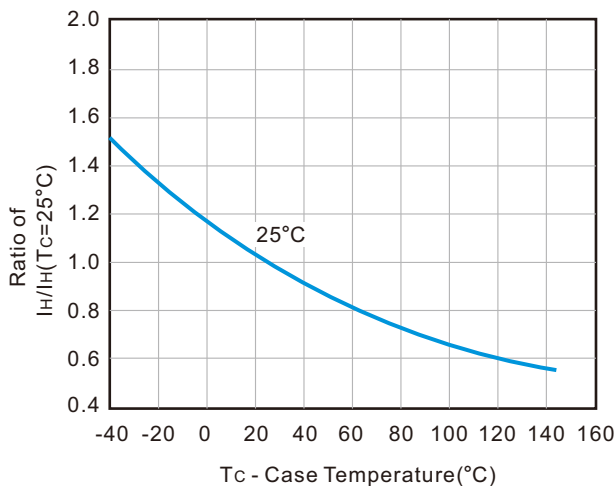
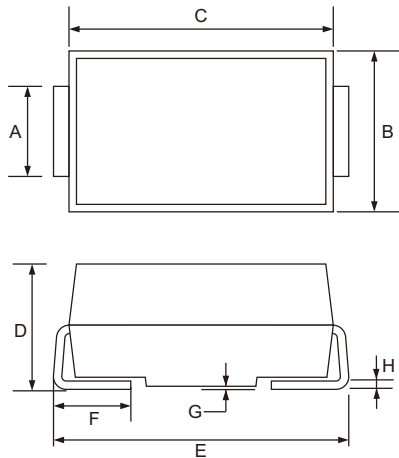


Fig.3 Normalized DC Holding Current versus Case Temperature



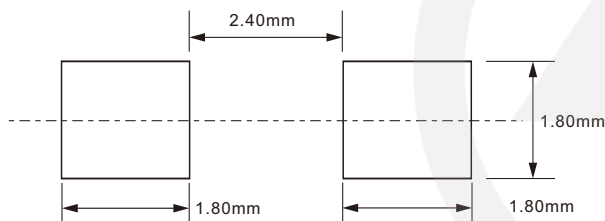
SMA Package Outline



Unit: mm

SYMBOL	DIMENSIONS	
	MIN.	MAX.
A	1.25	1.65
B	2.30	2.79
C	4.00	4.75
D	1.90	2.50
E	4.70	5.28
F	0.76	1.52
G	0.203 TYP.	
H	0.15	0.31

SMA Suggested Pad Layout

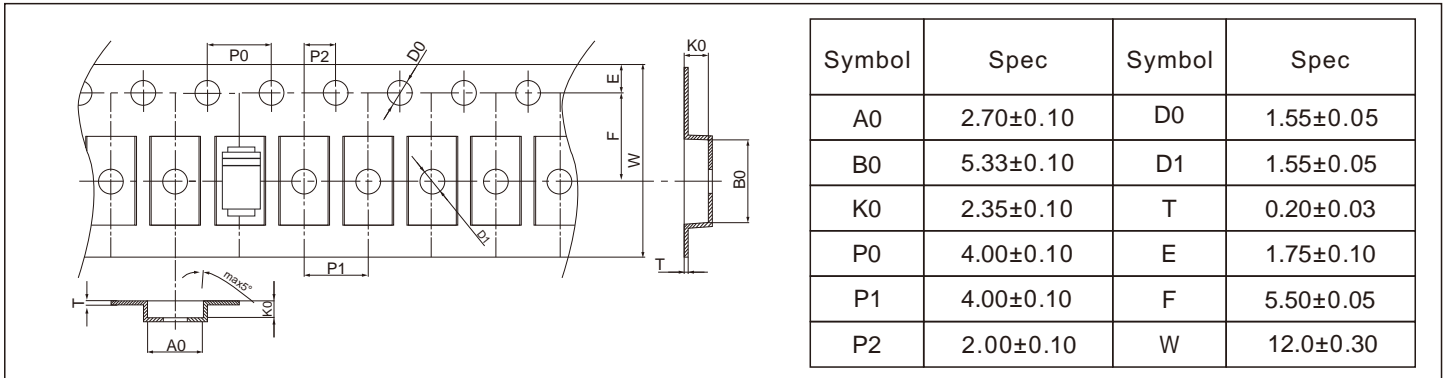


Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference purpose only.

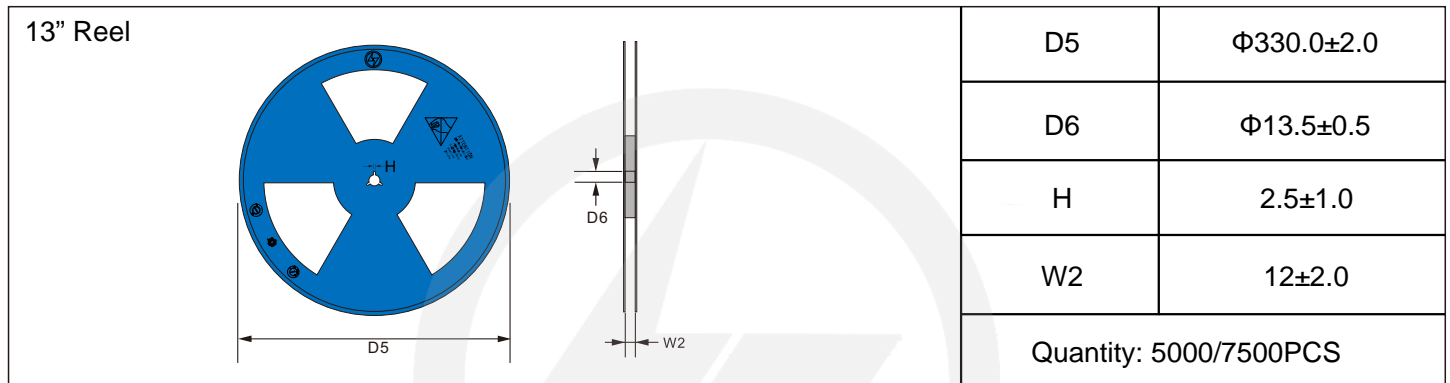
Carrier Tape Dimensions

Unit : mm

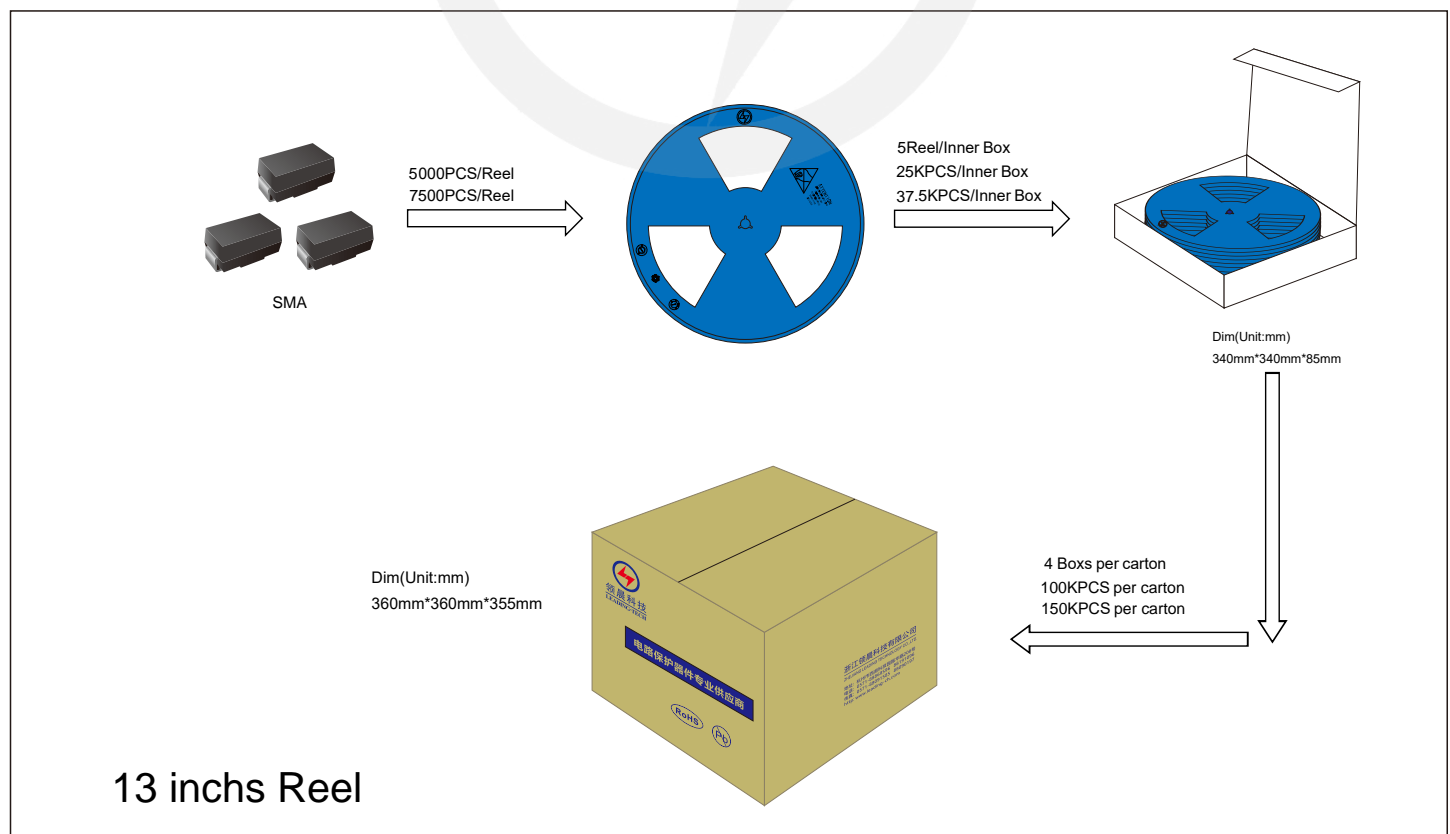


Reel Dimensions

Unit : mm

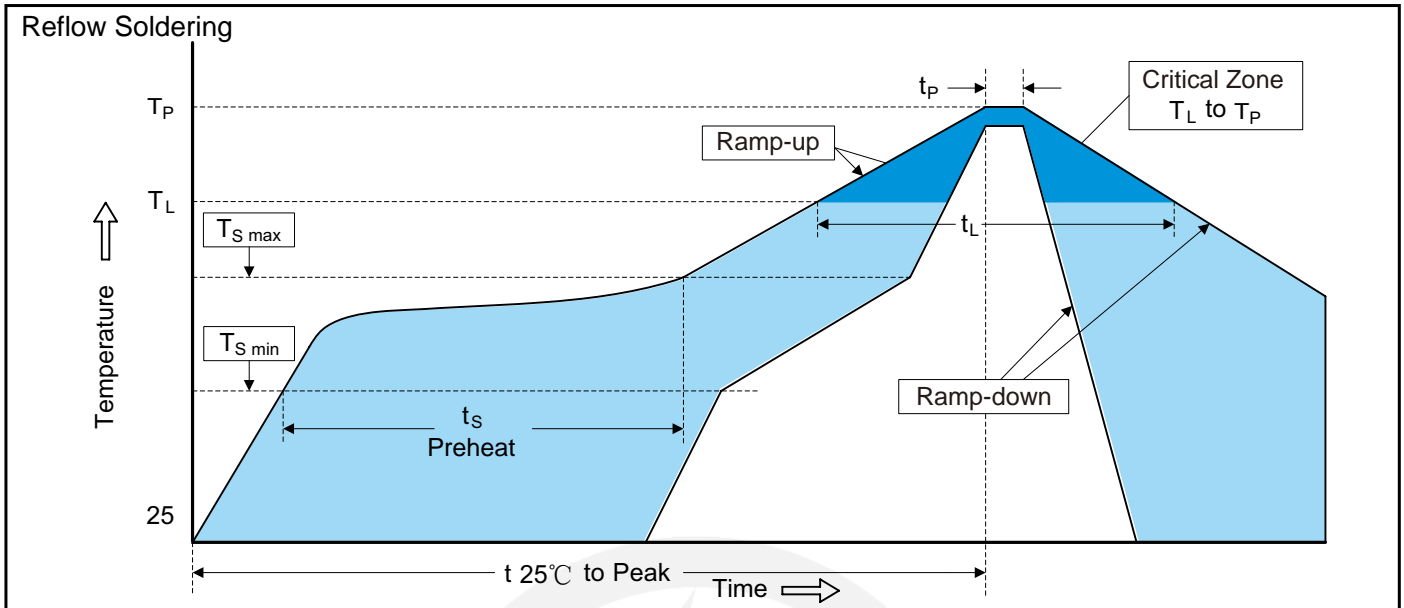


Packaging





Recommended Soldering Conditions



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T _L to T _P)	3°C/second max.
Preheat	
-Temperature Min (T _{S min})	150°C
-Temperature Max (T _{S max})	200°C
-Time (min to max) (t _s)	60-180 seconds
T _{S max} to T _L	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature (T _L)	217°C
-Time (t _L)	60-150 seconds
Peak Temperature (T _P)	260°C
Time within 5°C of actual Peak Temperature (t _p)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

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Version Update Information

Series NO.	Enactment/Revision Date	Effective Date	Version	Revision content	Revision Reason	Revision Person	Note
01	2025.01.21	2025.01.21	3.0	New File	/	Ding	