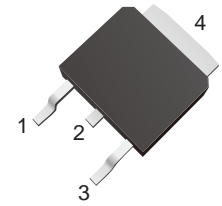


P-Channel Mosfet

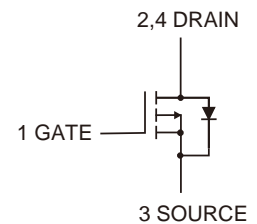
Features

- Advanced trench process technology
- Reliable and rugged
- Low On-Resistance
- Lead free in comply with EU RoHS 2011/65/EU directives



Ordering Information

Part Number	Marking	Shipping	Reel
LTM45P40-TR2K5	45P40	2500PCS Tape&Reel	13 inches



Maximum Ratings (Ta = 25°C)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current* ¹	-45	A
I_{DM}	Pulsed Drain Current $T_j=25^\circ\text{C}$ * ²	-180	A
E_{AS}	Single Pulsed Avalanche Energy* ³	130	mJ
P_D	Power Dissipation* ¹	73.5	W
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient* ⁶	62.5	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance from Junction to Case* ⁶	1.7	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 ~ +150	$^\circ\text{C}$

Electrical Characteristics (Ta = 25°C)

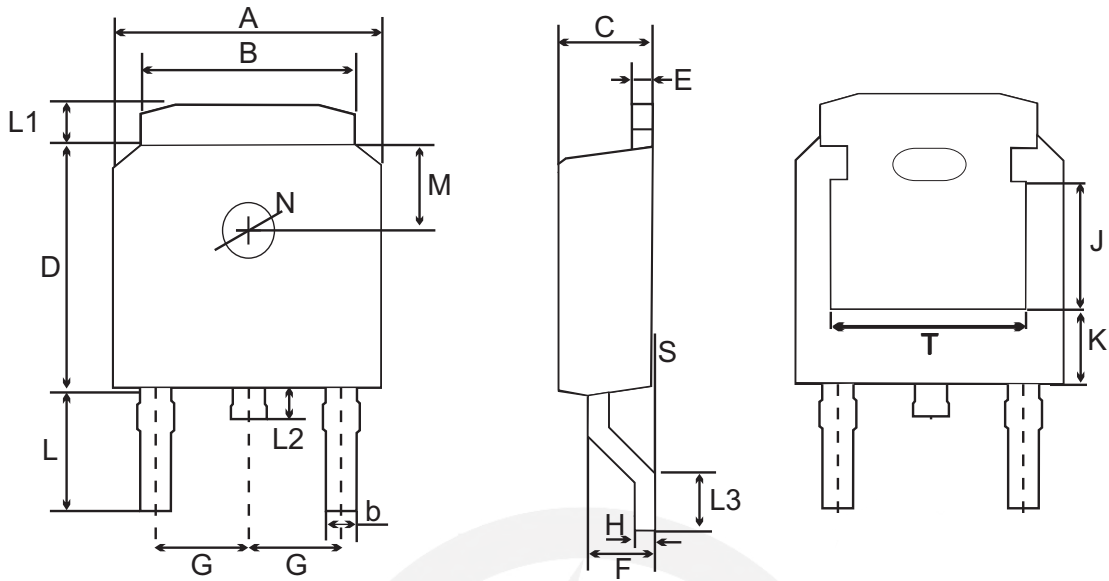
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off characteristics						
$V_{(BR)DSS}$	Drain-source breakdown voltage	$V_{GS} = 0V, I_D = -250\mu A$	-40			V
I_{DSS}	Zero gate voltage drain current	$V_{DS} = -40V, V_{GS} = 0V$			-1	μA
I_{GSS}	Gate-body leakage current	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
V_{SD}	Drain-source diode forward voltage*4	$V_{GS} = 0V, I_S = -20A$			-1.2	V
gfs	Forward Transconductance	$V_{DS} = -10V, I_D = -10A$		13		S
On characteristics *4						
$V_{GS(th)}$	Gate-threshold voltage	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.0	-1.6	-2.5	V
$R_{DS(on)}$	Static drain-source on-resistance	$V_{GS} = -10V, I_D = -20A$ $V_{GS} = -4.5V, I_D = -10A$		12 16	15 22	m Ω
Dynamic characteristics *4*5						
C_{ISS}	Input capacitance	$V_{DS} = -25V, V_{GS} = 0V,$ $f = 1MHz$		2757		pF
C_{OSS}	Output capacitance			240		
C_{RSS}	Reverse transfer capacitance			137		
Switching characteristics *4*5						
Qg	Total gate charge	$V_{DS} = -32V,$ $V_{GS} = -4.5V, I_D = -10A$		22.2		nC
Qgs	Gate-source charge			8.2		
Qgd	Gate-drain charge			8.8		
td(on)	Turn-on delay time	$V_{DS} = -20V, V_{GS} = -10V,$ $R_G = 6\Omega, I_D = -1A,$		23		ns
tr	Turn-on rise time			10		
td(off)	Turn-off delay time			135		
tf	Turn-off fall time			46		

Notes :

- *1. $T_C = 25^\circ C$ Limited only by maximum temperature allowed.
- *2. Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- *3. EAS condition: $V_{DD} = -25V, V_{GS} = -10V, L = 0.1mH, L_{AS} = -51A$ Starting $T_J = 25^\circ C$.
- *4. Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- *5. Guaranteed by design, not subject to production.
- *6. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a = 25^\circ C$.



TO-252 Package Outline



UNIT	A	B	b	C	D	E	F	G	H	L	L1	L2	L3	S	M	N	J	K	T	
mm	max	6.7	5.5	0.8	2.5	6.3	0.6	1.8	2.29 TYPICAL	0.55	3.1	1.2	1.0	1.75	0.23	1.8 TYPICAL	1.3 TYPICAL	3.16 ref.	1.80 ref.	4.83 ref.
	typ	6.6	5.3	0.7	2.3	6.1	0.5	1.5		0.50	2.8	1.0	0.8	1.30	0.15					
	min	6.3	5.1	0.3	2.1	5.9	0.4	1.3		0.45	2.7	0.8	0.6	1.00	0.0					



Recommended Soldering Conditions



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T _L to T _P)	3°C/second max.
Preheat	
-Temperature Min (T _{S min})	150°C
-Temperature Max (T _{S max})	200°C
-Time (min to max) (t _s)	60-180 seconds
T _{S max} to T _L	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature (T _L)	217°C
-Time (t _L)	60-150 seconds
Peak Temperature (T _P)	260°C
Time within 5°C of actual Peak Temperature (t _p)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

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Version Update Information

Series NO.	Enactment/Revision Date	Effective Date	Version	Revision content	Revision Reason	Revision Person	Note
01	2024.9.14	2024.9.14	3.0	New File	/	Ding	
02	2025.06.11	2025.06.11	3.1	Update packaging information	/	Ding	