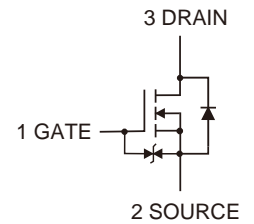
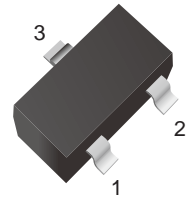


N-Channel Mosfet

Features

- High Density Cell Design for Low RDS(ON)
- Voltage Controlled Small Signal Switch
- Small Outline Surface Mount Package
- Lead free in comply with EU RoHS 2011/65/EU directives



Ordering Information

Part Number	Marking	Shipping	Reel
LTM3134KT-TR3	KF	3000PCS Tape&Reel	7 inchs
LTM3134KT-TR8	KF	8000PCS Tape&Reel	7 inchs

Maximum Ratings (Ta=25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D^*	Drain Current-Continuous	0.75	A
I_{DM}^*	Pulsed Drain Current ($V_{GS}=10V, tp=10\mu s$)	3	A
P_D	Power Dissipation	0.15	W
$R_{\theta JA}$	Thermal Resistance From Junction To Ambient	833	$^{\circ}C/W$
T_j	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature	-55~+150	$^{\circ}C$

* Surface Mounted on 1 in² pad area, t ≤ 10 sec

Electrical Characteristics (@ 25°C Unless Otherwise Specified)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20			V
$V_{GS(th)}$	Gate Threshold Voltage ^{*1}	$V_{DS}=V_{GS}, I_D=250\mu A$	0.35	0.75	1.1	V
I_{GSS}	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 10V$			± 20	μA
I_{DSS}	Drain Leakage Current	$V_{DS}=20V, V_{GS}=0V$			1	μA
$R_{DS(ON)}$	On-State Resistance ^{*1}	$V_{GS}=4.5V, I_D=650mA,$ $V_{GS}=2.5V, I_D=550mA,$ $V_{GS}=1.8V, I_D=450mA$			0.38 0.45 0.8	Ω
V_{SD}	Diode Forward Voltage ^{*1}	$V_{GS}=0V, I_S=150mA$			1.2	V
gfs	Forward Transconductance	$V_{DS}=10V, I_D=800mA$	1.0			mS

Dynamic Characteristics^{*2}

C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=16V, f=1MHz$			120	pF
C_{oss}	Output Capacitance				20	
C_{riss}	Reverse Transfer Capacitance				15	

Switching Characteristics^{*2}

$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=10V, R_G=10\Omega$ $V_{GS}=4.5V, I_D=500mA$		6.7		nS
t_r	Turn-on Rise Time			17.3		
$t_{d(off)}$	Turn-off Delay Time			4.8		
t_f	Turn-off Fall Time			7.4		

*1. Pulse Test : Pulse Width \leq 300 s, Duty Cycle \leq 0.5%.

*2. These parameters have no way to verify.



Characteristics Curves

Fig.1 Output Characteristics

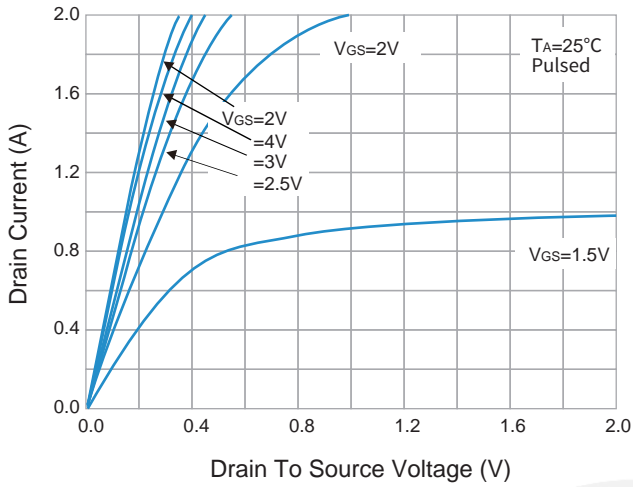


Fig.2 Transfer Characteristics

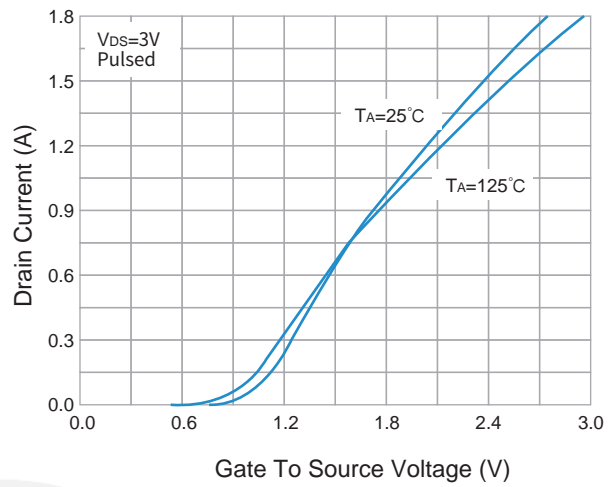


Fig.3 $R_{DS(ON)}$ vs I_D

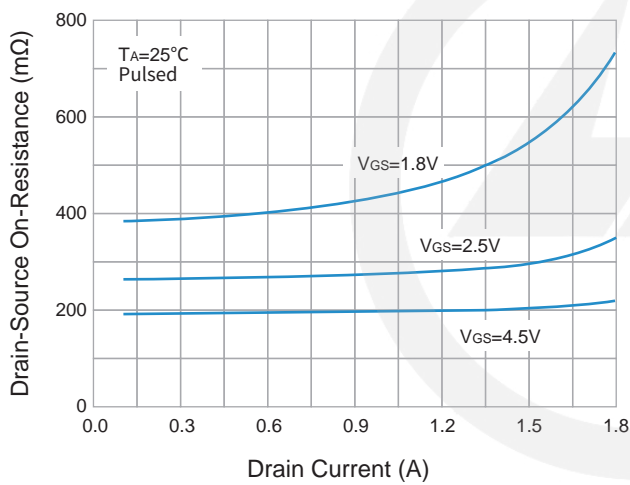


Fig.4 $R_{DS(ON)}$ vs V_{GS}

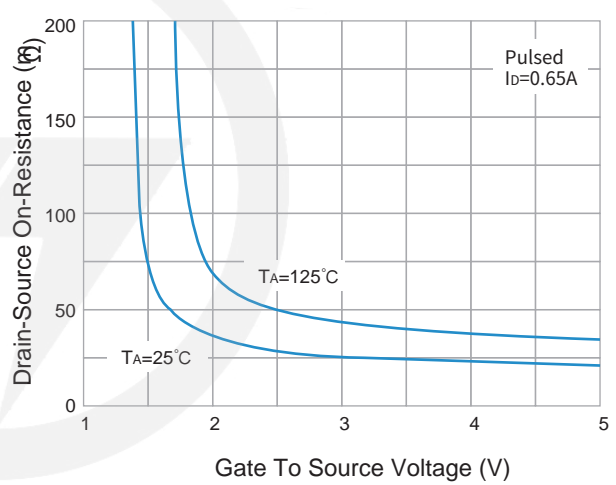


Fig.5 I_S vs V_{SD}

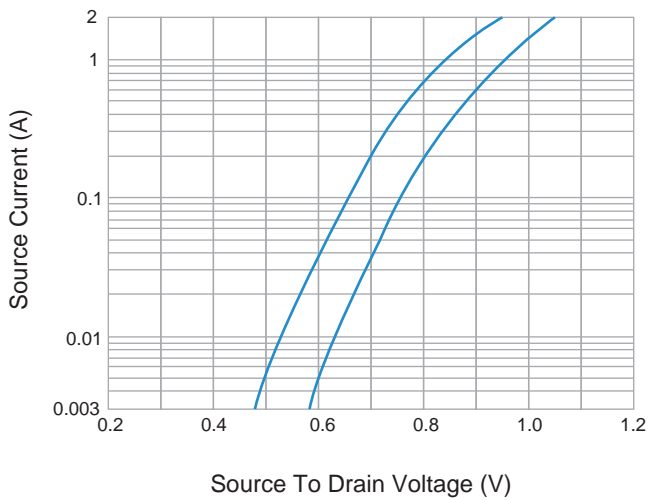
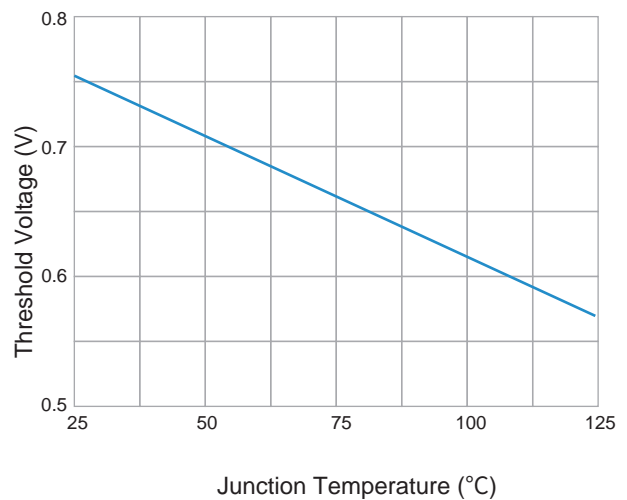
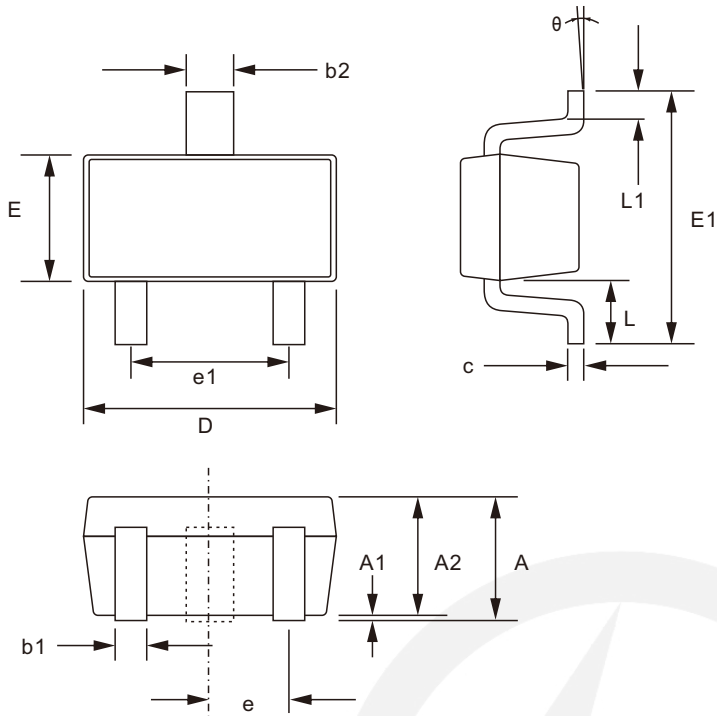


Fig.6 Threshold Voltage





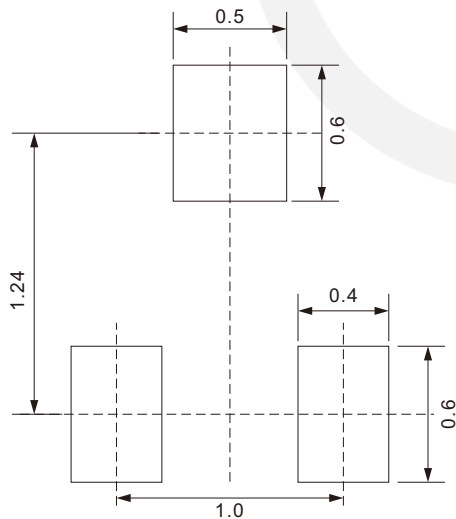
SOT-523 Package Outline



Unit: mm

SYMBOL	DIMENSIONS	
	MIN.	MAX.
A	0.70	0.90
A1	0.00	0.10
A2	0.70	0.80
b1	0.15	0.25
b2	0.25	0.35
c	0.10	0.20
D	1.50	1.70
E	0.70	0.90
E1	1.45	1.75
e	0.50 TYP.	
e1	0.90	1.10
L	0.40 TYP.	
L1	0.10	0.30
theta	0°	8°

SOT-523 Suggested Pad Layout

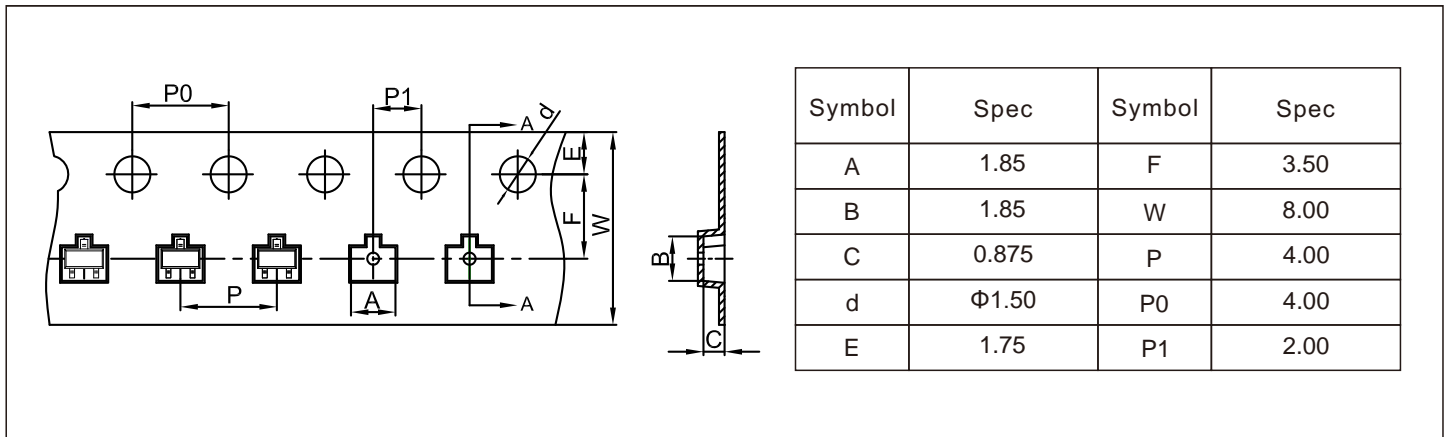


Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference purpose only.

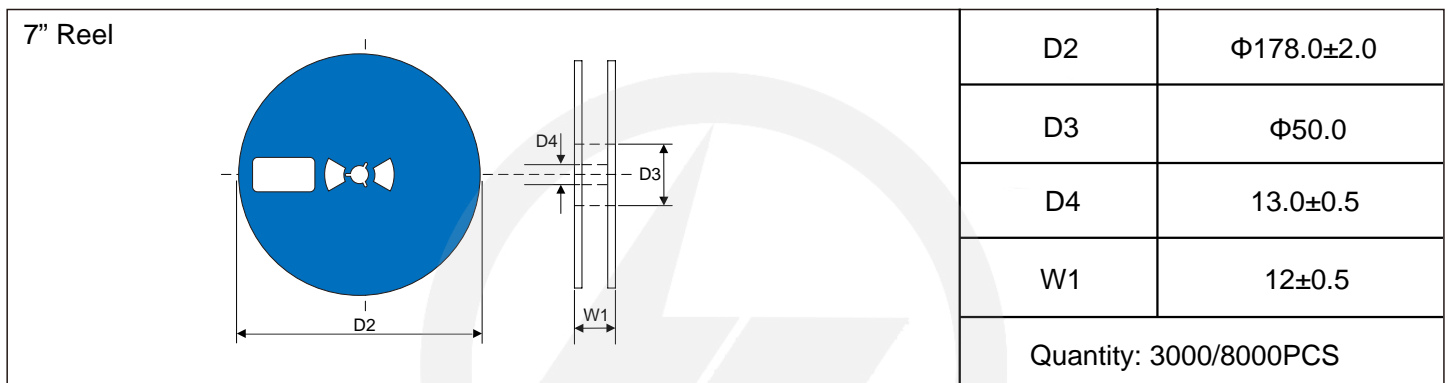
Carrier Tape Dimensions

Unit : mm

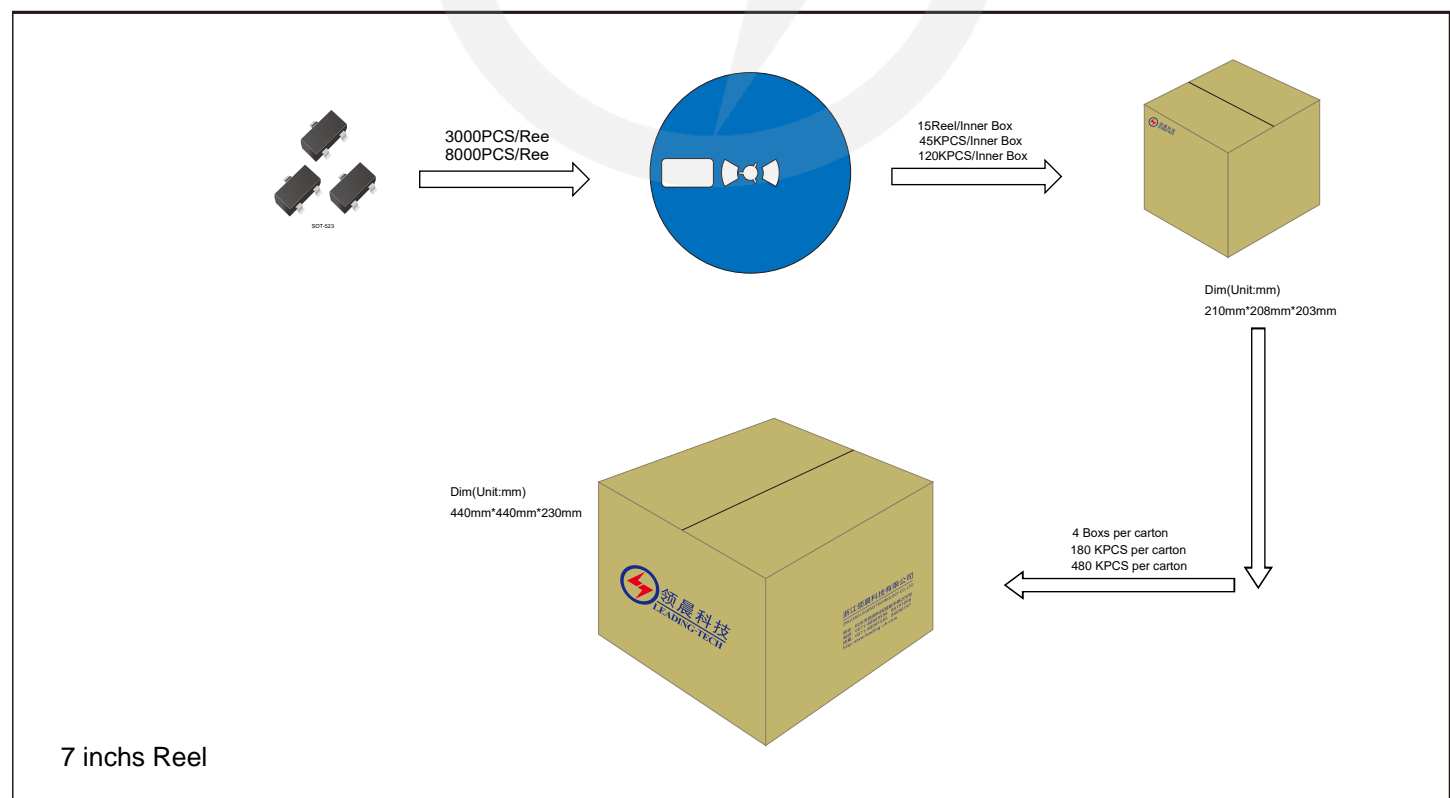


Reel Dimensions

Unit : mm

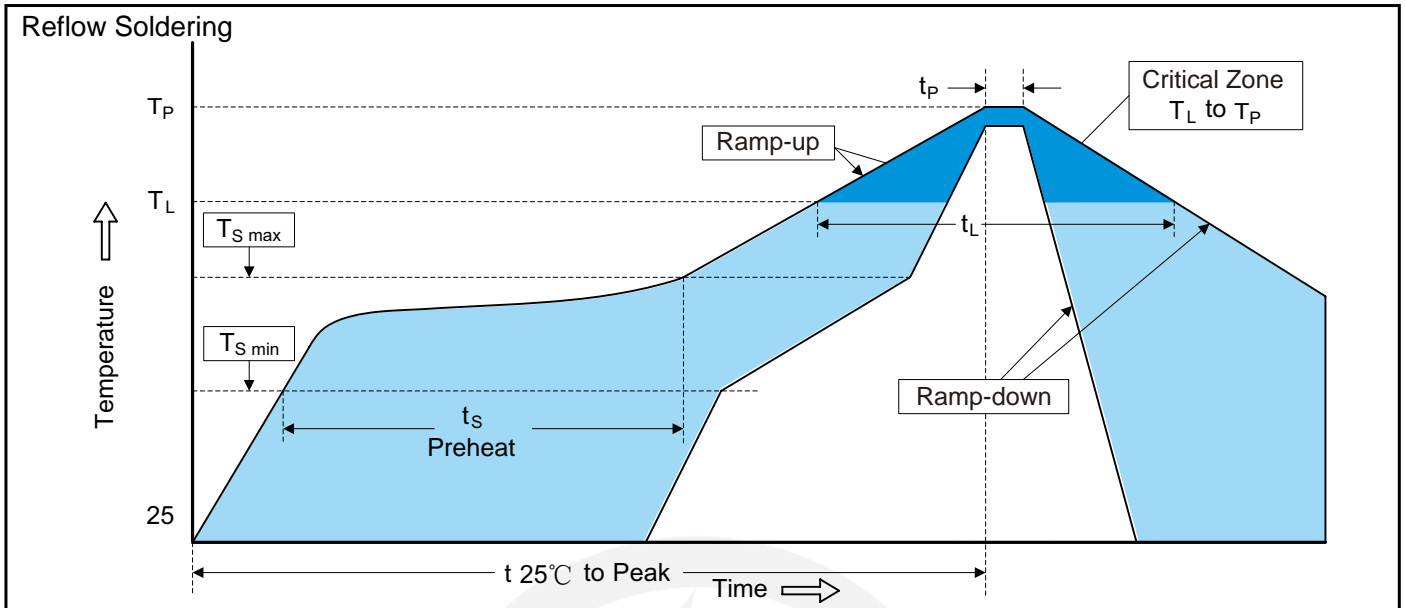


Packaging





Recommended Soldering Conditions



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T _L to T _P)	3°C/second max.
Preheat	
-Temperature Min (T _{S min})	150°C
-Temperature Max (T _{S max})	200°C
-Time (min to max) (t _s)	60-180 seconds
T _{S max} to T _L	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature (T _L)	217°C
-Time (t _L)	60-150 seconds
Peak Temperature (T _P)	260°C
Time within 5°C of actual Peak Temperature (t _p)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

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Version Update Information

Series NO.	Enactment/Revision Date	Effective Date	Version	Revision content	Revision Reason	Revision Person	Note
01	2024.3.1	2024.3.1	3.0	New File	/	Ding	