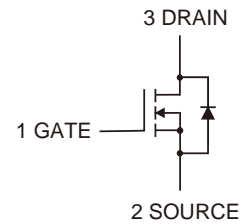
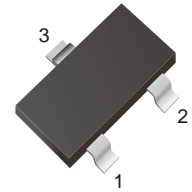


## N-Channel Mosfet

### Features

- 50V, 0.22A  
 $R_{DS(ON)}=3.5\Omega @V_{GS}=10V$   
 $R_{DS(ON)}=6.0\Omega @V_{GS}=4.5V$
- Rugged and Reliable
- High Density Cell Design for Extremely  $R_{DS(ON)}$
- Compact Industry Standard SOT-23 Surface Mount Package
- This Device is Pb-Free and Halogen Free
- Lead free in comply with EU RoHS 2011/65/EU directives



### Mechanical Data

- Case:SOT-23
- Approx. Weight: 8.1mg

### Ordering Information

Part Number	Marking	Shipping	Reel
LTM138N-TR3	SS138	3000PCS Tape&Reel	7 inchs
LTM138N-TR12	SS138	12000PCS Tape&Reel	13 inchs

### Absolute Maximum Ratings (TA=25°C)

Parameter	Symbols	Ratings	Units
Drain-Source Voltage	$V_{DSS}$	50	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	0.22	A
Pulsed Drain Current		0.88	A
Power Dissipation	$P_D$	0.36	W
Thermal Resistance - Junction to Case <sup>(1)</sup>	$R_{\theta JA}$	350	°C/W
Operation Junction Temperature and Storage Temperature	$T_j, T_{stg}$	-55~+150	°C

#### Notes:

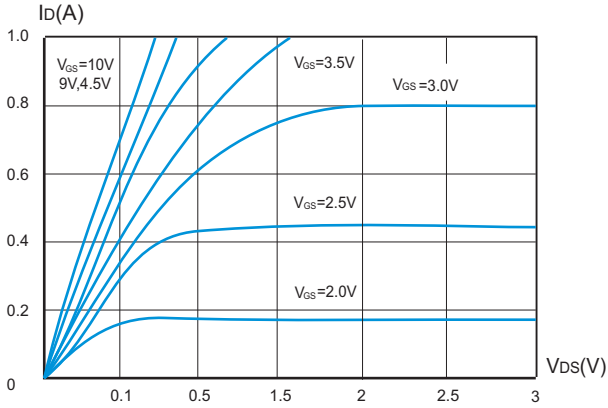
1.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta JC}$  is guaranteed by design, while  $R_{\theta JA}$  is determined by the board design, The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.

**Electrical Characteristics (TA=25°C)**

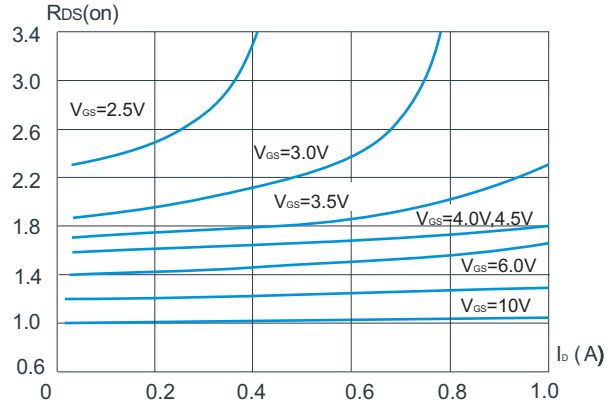
Parameter	Symbols	Text conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	B <sub>VDS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	50			V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V			0.5	uA
		V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			100	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =1mA	0.8	1.3	1.5	V
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =0.22A		0.7	3.5	Ω
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.22A		1.0	6.0	Ω
On-State Drain Current	I <sub>D(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =5V	0.2			A
Forward Transconductance	g <sub>FS</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =0.22A	0.12	0.5		S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25 V, V <sub>GS</sub> =0V, f=1.0MHz		27		pF
Output Capacitance	C <sub>oss</sub>			13		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			6		pF
Gate Resistance	R <sub>G</sub>	V <sub>GS</sub> =15mV, f=1.0MHz		9		Ω
<b>Switching Characteristics</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =25 V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.22A		1.7	2.4	nC
Gate-Source Charge	Q <sub>gs</sub>			0.1		nC
Gate-Drain Charge	Q <sub>gd</sub>			0.4		nC
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V, R <sub>GEN</sub> =6Ω, V <sub>GS</sub> =10V, I <sub>D</sub> =0.29A		2.5	5	ns
Turn-On Rise Time	t <sub>r</sub>			9	18	ns
Turn-Off Delay Time	t <sub>d(off)</sub>			20	36	ns
Turn-Off Fall Time	t <sub>f</sub>			7	14	ns
<b>Source-Drain Diode Characteristics</b>						
Maximum Continuous Drain-Source Forward Current	I <sub>S</sub>				0.22	A
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =0.44A		0.8	1.4	V

**Characteristics Curves**

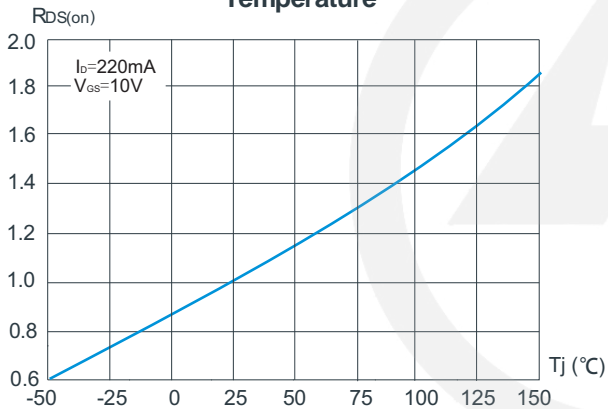
**Fig.1 On-Region Characteristics**



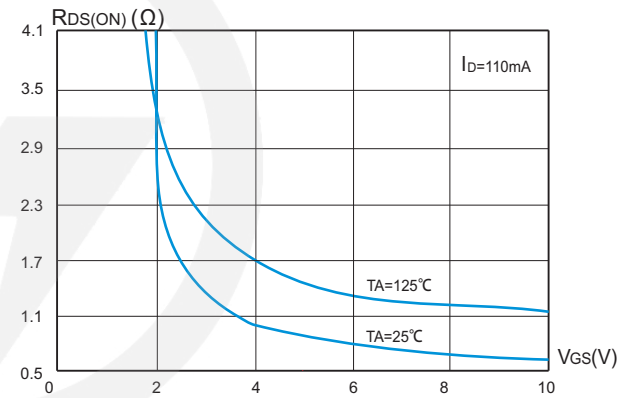
**Fig.2 On-Resistance Variation with Drain Current and Gate Voltage**



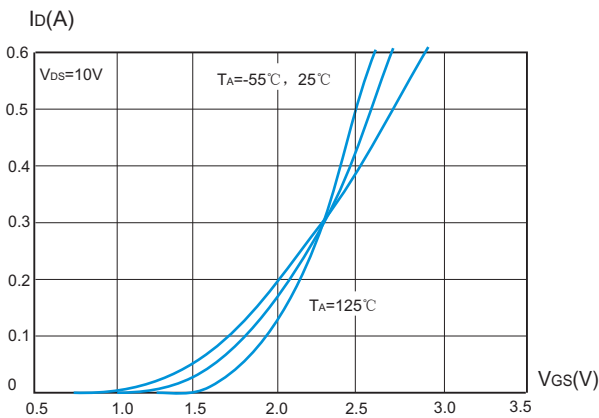
**Fig.3 On-Resistance Variation with Temperature**



**Fig.4 On-resistance variation with Gate-to-Source voltage**



**Fig.5 Transfer Characteristics**



**Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature**

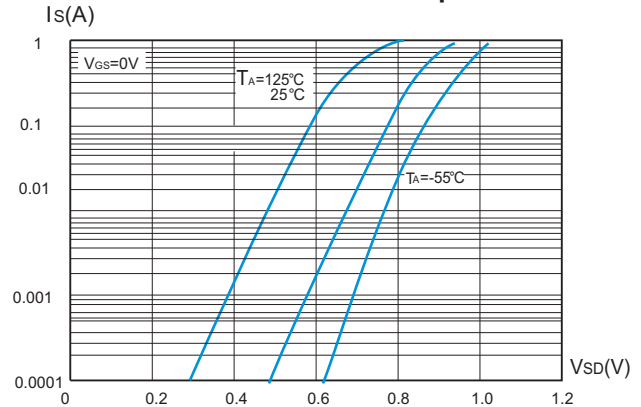




Fig.7 Gate Charge Characteristics

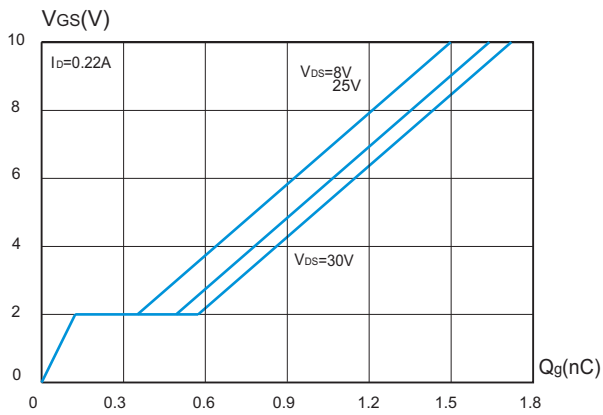


Fig.8 Capacitance Characteristics

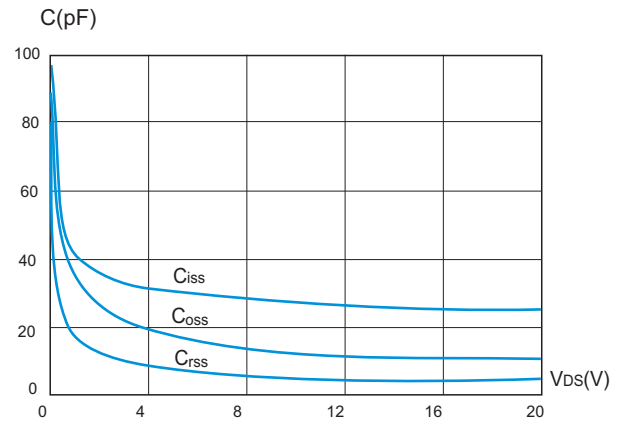


Fig.9 Maximum Safe Operating Area

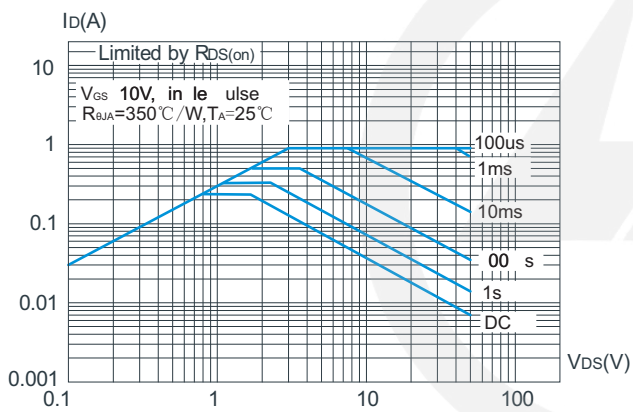
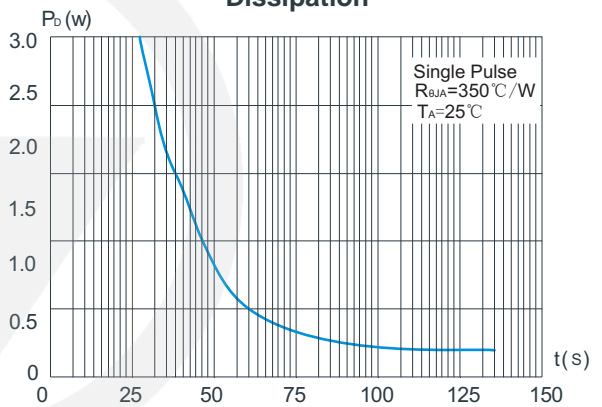
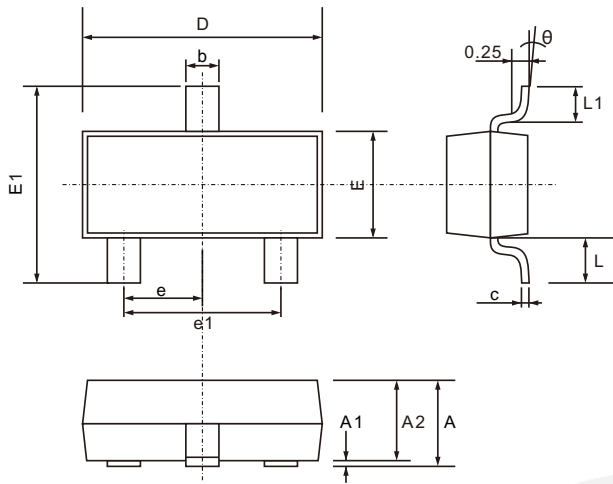


Fig.10 Single Pulse Maximum Power Dissipation



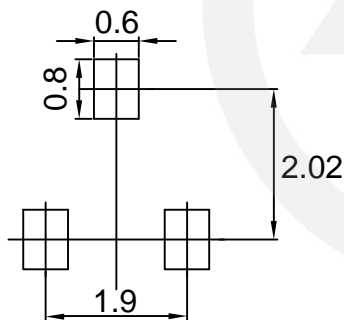
## SOT-23 Package Outline

Unit: mm



SYMBOL	DIMENSIONS	
	MIN.	MAX.
A	0.900	1.200
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.200
D	2.700	3.100
E	1.200	1.400
E1	2.200	2.600
e	0.950 TYP.	
e1	1.750	2.050
L	0.550 TYP.	
L1	0.300	0.500
$\theta$	0°	8°

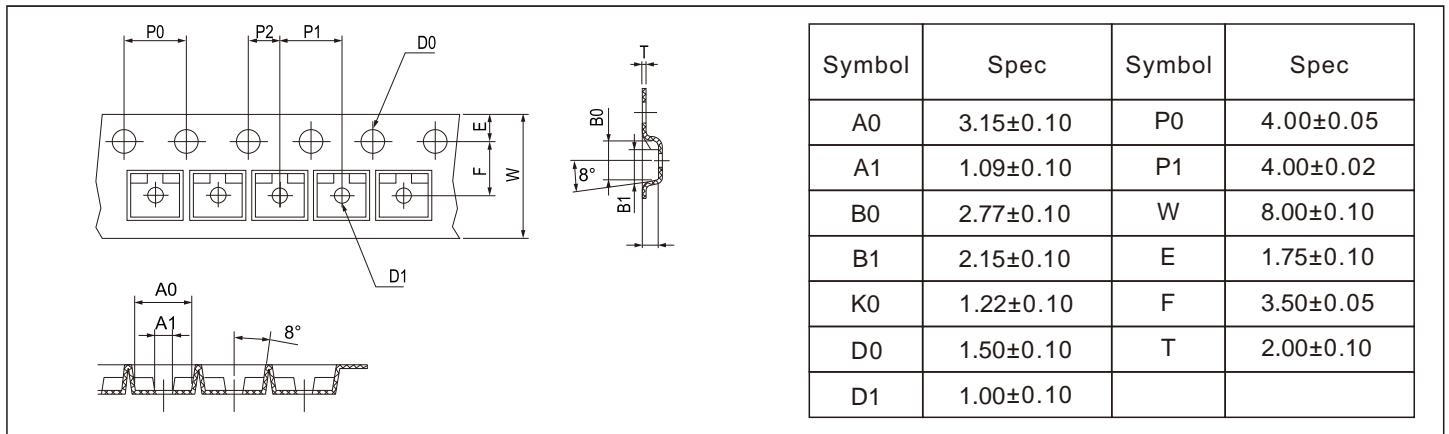
## SOT-23 Suggested Pad Layout



- Note:
1. Controlling dimension: in millimeters.
  2. General tolerance:  $\pm 0.05\text{mm}$
  3. The pad layout is for reference purpose only.

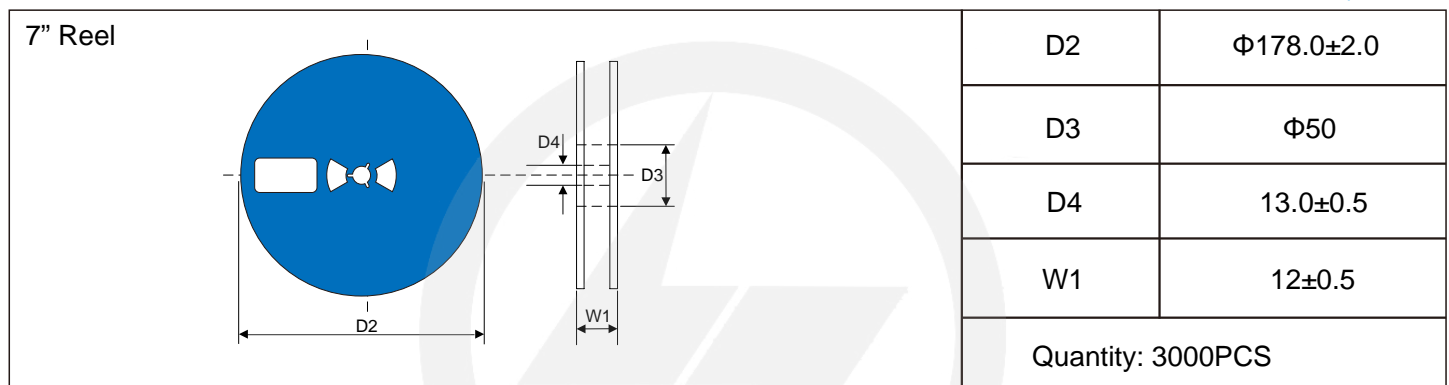
## Carrier Tape Dimensions

Unit : mm



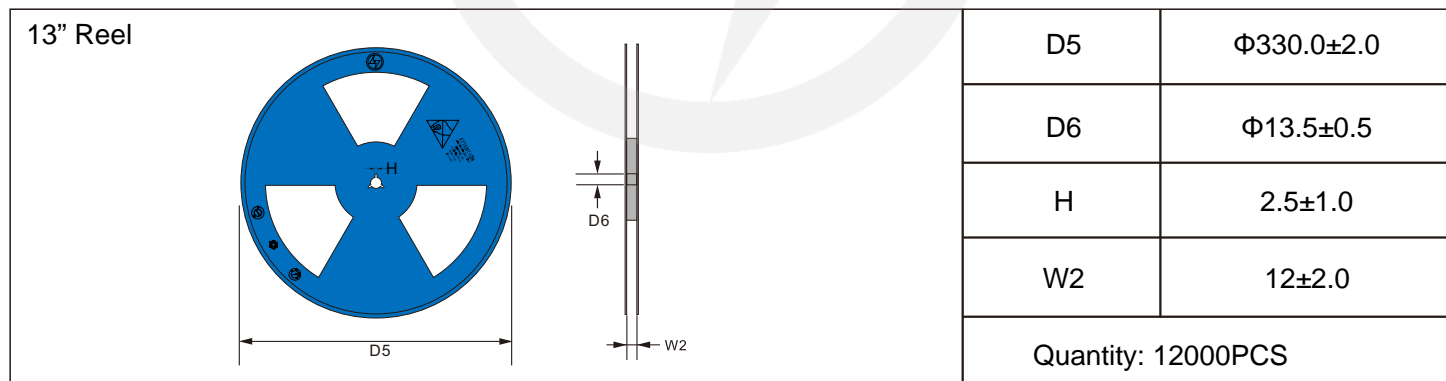
## Reel Dimensions

Unit : mm

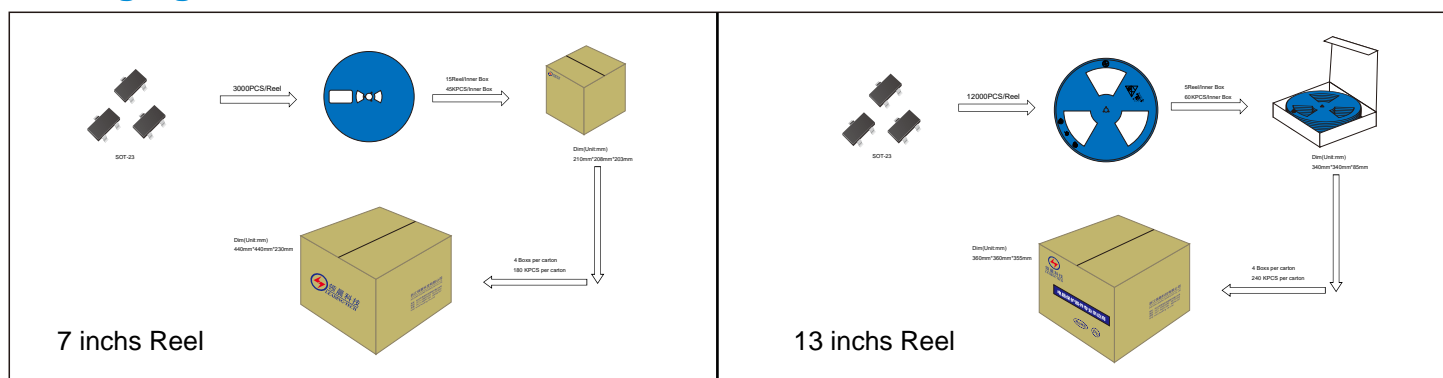


## Reel Dimensions

Unit : mm

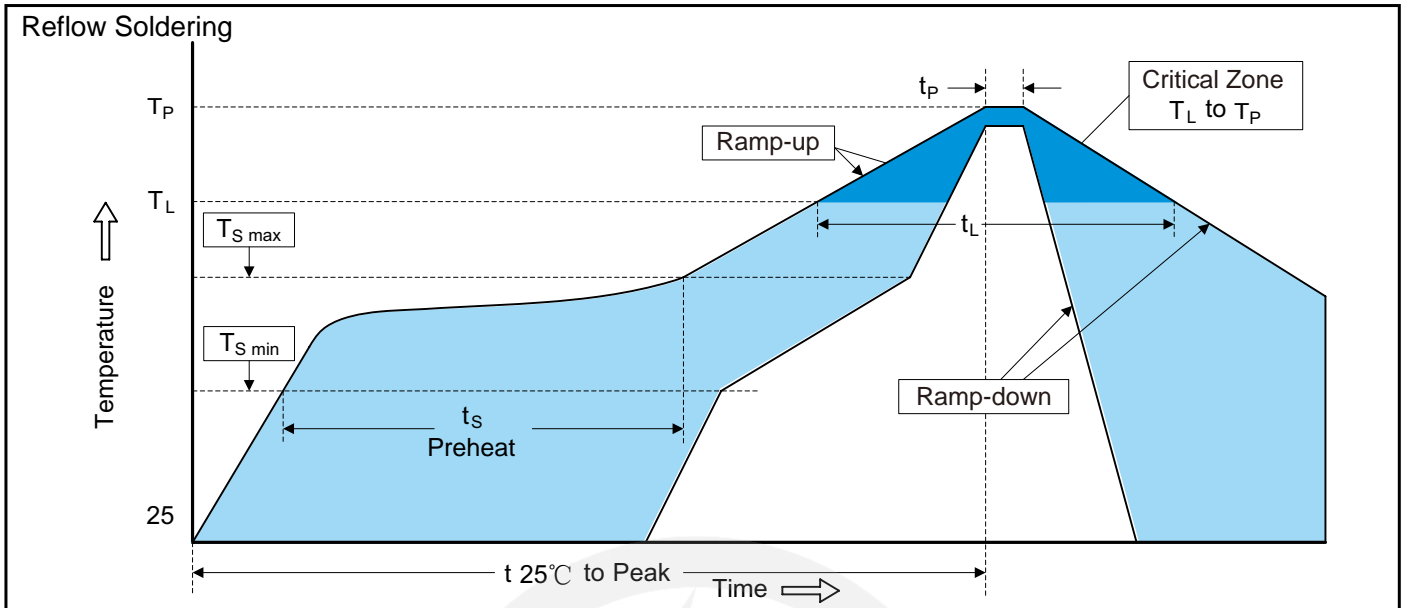


## Packaging





Recommended Soldering Conditions



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second max.
Preheat	
-Temperature Min (T <sub>S min</sub> )	150°C
-Temperature Max (T <sub>S max</sub> )	200°C
-Time (min to max) (t <sub>s</sub> )	60-180 seconds
T <sub>S max</sub> to T <sub>L</sub>	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature (T <sub>L</sub> )	217°C
-Time (t <sub>L</sub> )	60-150 seconds
Peak Temperature (T <sub>P</sub> )	260°C
Time within 5°C of actual Peak Temperature (t <sub>p</sub> )	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

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## Version Update information

Series NO.	Enactment/Revision Date	Effective Date	Version	Revision Content	Revision Reason	Revision Person	Note
01	2025.03.19	2025.03.19	3.0	New file	/	Ding	
02	2026.03.04	2026.03.04	3.1	Package outline E1(max)=2.6mm	/	Ding	