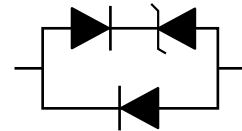




1-Line Low Capacitance TVS Diode

Features

- Protects one data line
- Ultra low leakage: nA level
- Operating voltage: 5V
- Ultra low clamping voltage
- Ultra low capacitance: 0.6pF
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 25\text{kV}$
 - Contact discharge: $\pm 20\text{kV}$
 - IEC61000-4-5 (Lightning) 5A (8/20 μs)
- Lead free in comply with EU RoHS 2011/65/EU directives



Mechanical Data

- Package: SOD-523
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram Below

Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks, Desktops, Server
- USB 2.0 and USB 3.0 Interfaces
- Ethernet - 10/100/1000 Base T

Ordering Information

Part Number	Marking	Shipping	Reel
LTE5L05A01LN-TR3	5Y	3000PCS Tape&Reel	7 inches
LTE5L05A01LN-TR10	5Y	10000PCS Tape&Reel	7 inches



Absolute Maximum Rating (TA=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	75	W
Peak Pulse Current (8/20μs)	IPP	5	A
ESD per IEC 61000-4-2 (Air)	VESD	±25	kV
ESD per IEC 61000-4-2 (Contact)		±20	
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Tstg	-55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	
Breakdown Voltage	VBR	6			V	IT = 1mA
Reverse Leakage Current	IR			0.5	μA	VRWM = 5V
Clamping Voltage	VC			10	V	IPP = 1A (8 x 20μs pulse)
Clamping Voltage	VC			15	V	IPP = 5A (8 x 20μs pulse)
Junction Capacitance	CJ		0.6	0.8	pF	VR = 0V, f = 1MHz



Characteristics Curves

Fig.1 Junction Capacitance vs Reverse Voltage

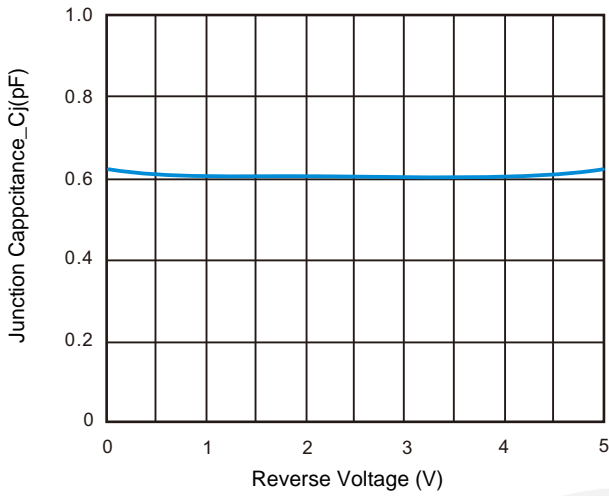


Fig.2 Peak Pulse Power vs Pulse Time

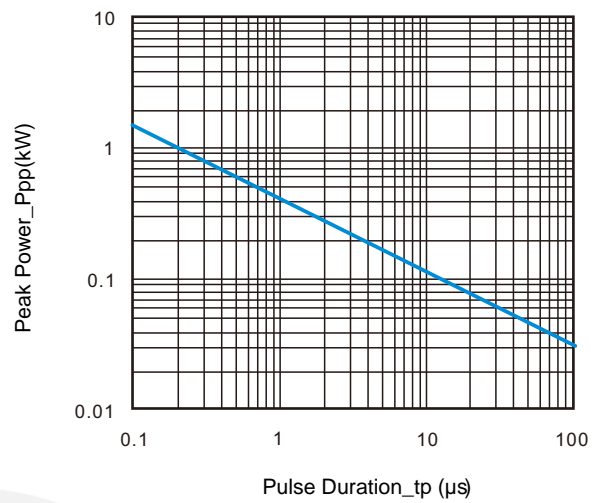


Fig.3 Clamping Voltage vs Peak Pulse Current

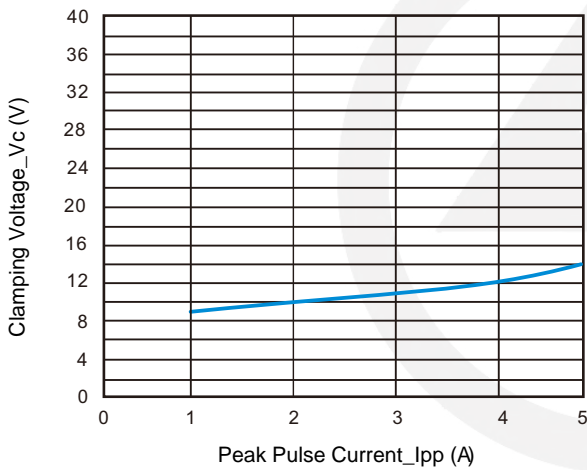


Fig.4 Power Derating Curve

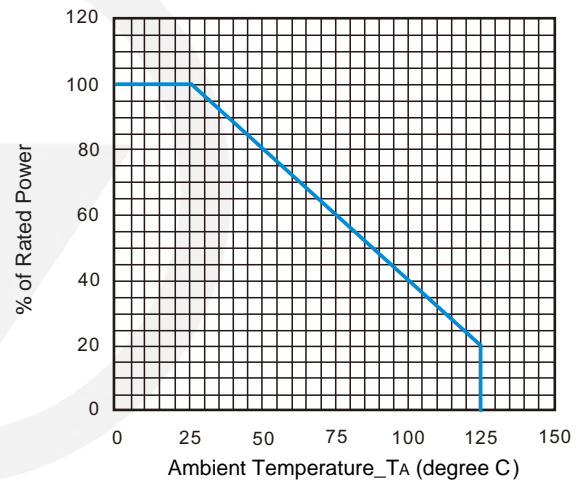


Fig.5 8x20μs Pulse Waveform

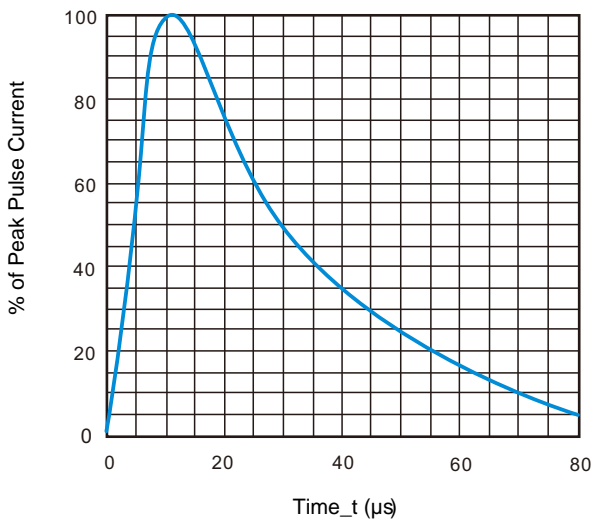
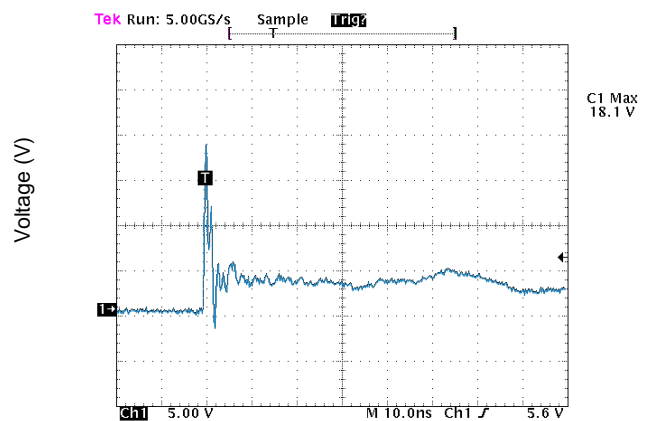


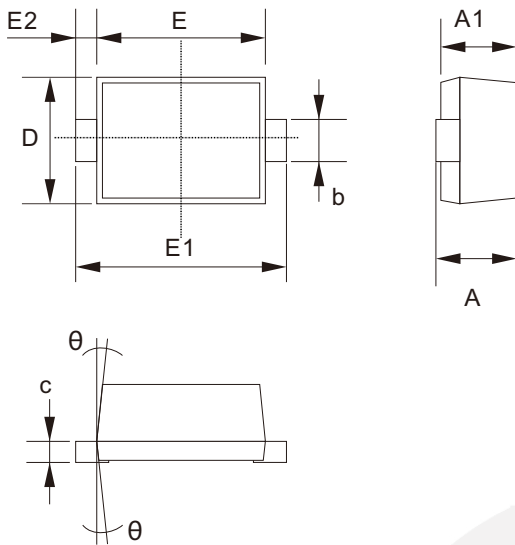
Fig.6 ESD Clamping Voltage 8kV Contact per IEC61000-4-2



Note: Data is taken with a 10x attenuator

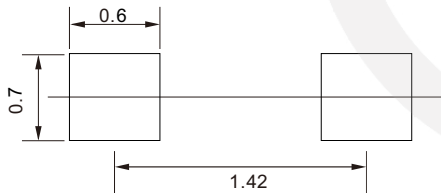
SOD-523 Package Outline

Unit: mm



SYMBOL	DIMENSIONS	
	MIN.	MAX.
A	0.500	0.770
A1	0.500	0.700
b	0.250	0.380
c	0.070	0.200
D	0.700	0.900
E	1.100	1.300
E1	1.500	1.700
E2	0.200 REF	
θ	7° REF	

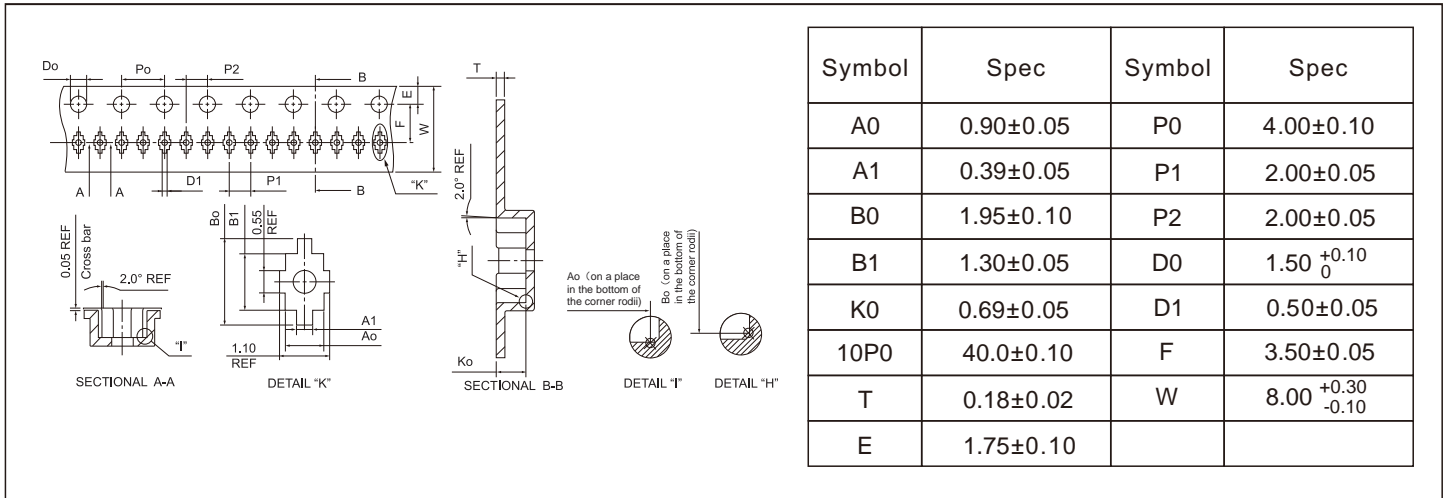
SOD-523 Suggested Pad Layout



- Note:
1. Controlling dimension: in millimeters.
 2. General tolerance: ± 0.05 mm.
 3. The pad layout is for reference purposes only.

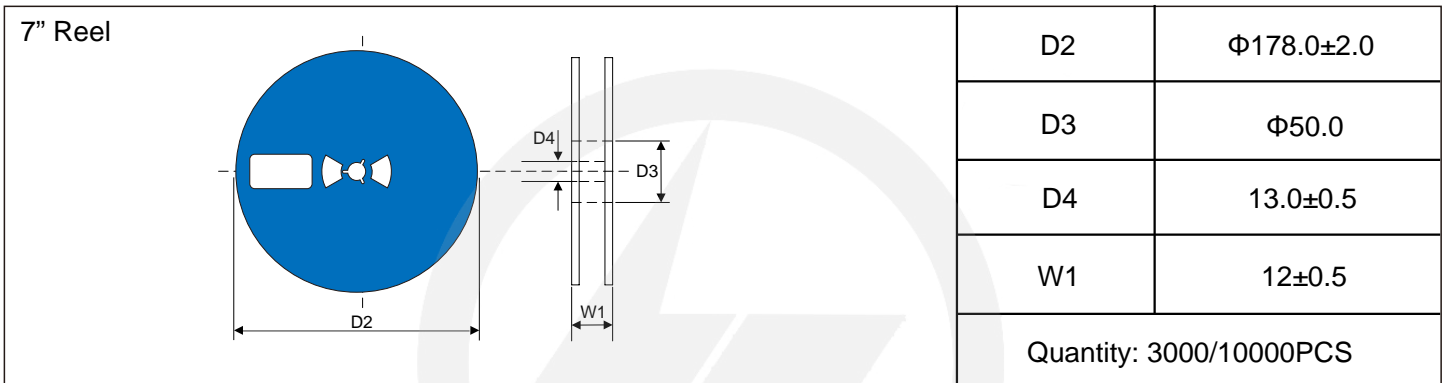
Carrier Tape Dimensions

Unit : mm

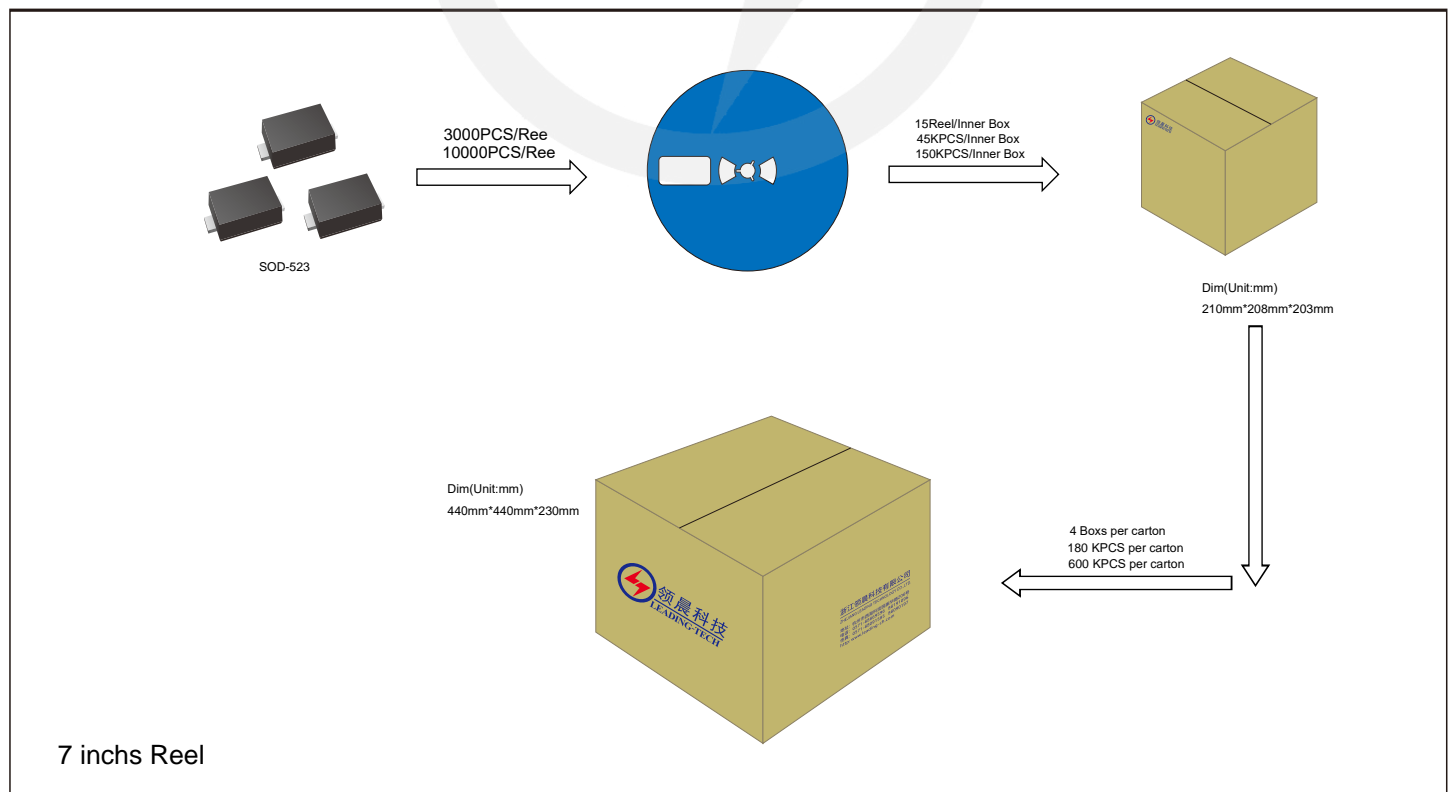


Reel Dimensions

Unit : mm

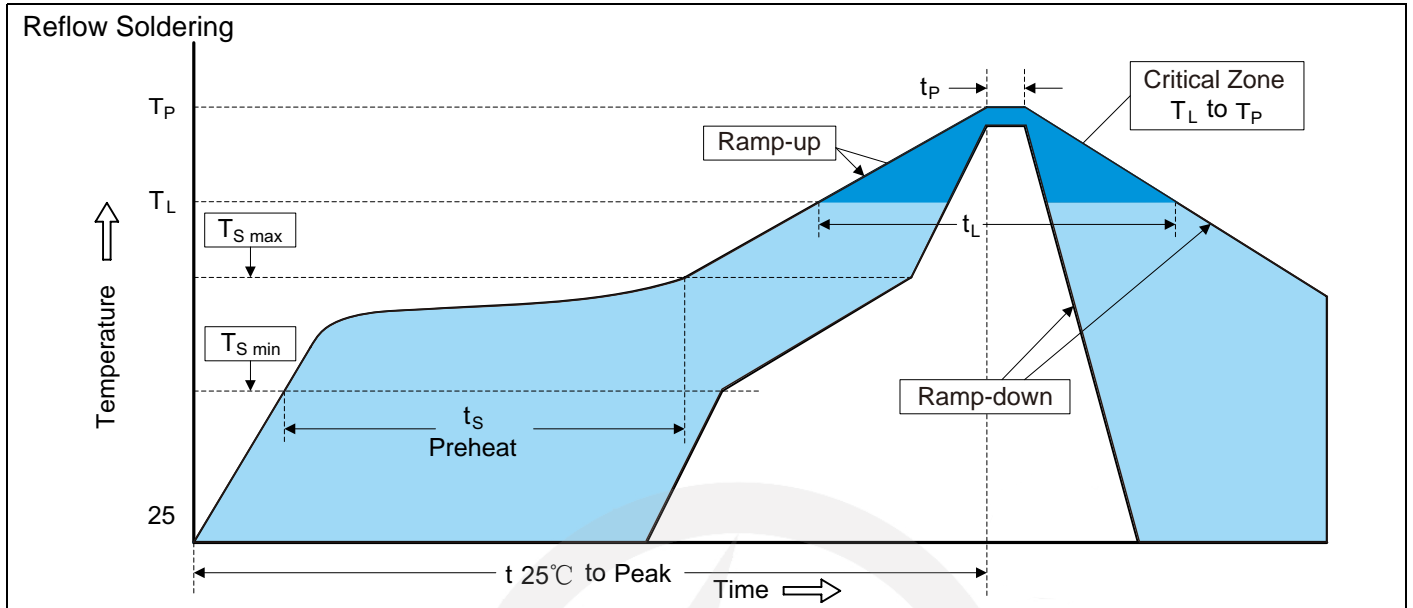


Packaging





Recommended Soldering Conditions



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.
Preheat	
-Temperature Min ($T_{S\ min}$)	150°C
-Temperature Max ($T_{S\ max}$)	200°C
-Time (min to max) (t_s)	60-180 seconds
$T_{S\ max}$ to T_L	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature (T_L)	217°C
-Time (t_L)	60-150 seconds
Peak Temperature (T_P)	260°C
Time within 5°C of actual Peak Temperature (t_P)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

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Version Update Information

Series NO.	Enactment/Revision Date	Effective Date	Version	Revision content	Revision Reason	Revision Person	Note
01	2024.4.5	2024.4.5	3.0	New File	/	Ding	