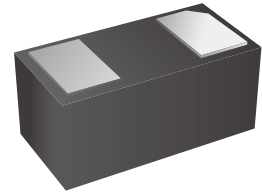




1-Line Ultra Low Capacitance Bi-directional TVS Diode

Features

- Ultra small package: 1.0x0.6x0.5mm
- Ultra low capacitance: 0.3pF typical
- Ultra low leakage: nA level
- Operating voltage: 5V
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
Air discharge: $\pm 25\text{kV}$
Contact discharge: $\pm 25\text{kV}$
 - IEC61000-4-5 (Lightning) 5A (8/20 μs)
- Lead free in comply with EU RoHS 2011/65/EU directives



Mechanical Data

- Case: DFN1006
- Case Material: "Green" Molding Compound
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram Below

Applications

- Cellular Handsets and Accessories
- Display Ports
- MDDI Ports
- USB Ports
- Digital Visual Interface (DVI)
- PCI Express and Serial SATA Ports

Ordering Information

Part Number	Marking	Shipping	Reel
LTE10N05C01LV-TR10	2V	10000PCS Tape&Reel	7 inchs

Absolute Maximum Ratings (Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	80	W
Peak Pulse Current (8/20μs)	IPP	5	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	VESD	±25 ±25	kV
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Tstg	-55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	
Breakdown Voltage	VBR	6			V	IT = 1mA
Reverse Leakage Current	IR			0.2	μA	VRWM = 5V
Clamping Voltage	VC			10	V	IPP = 1A (8 x 20μs pulse)
Clamping Voltage	VC			16	V	IPP = 5A (8 x 20μs pulse)
Junction Capacitance	CJ		0.3		pF	VR = 0V, f = 1MHz



Characteristic Curves

Fig.1 Junction Capacitance vs Reverse Volatage

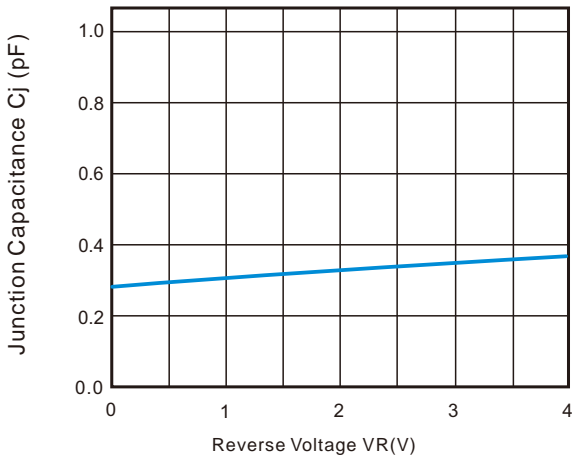


Fig.2 Peak Pulse Power vs Pulse Time

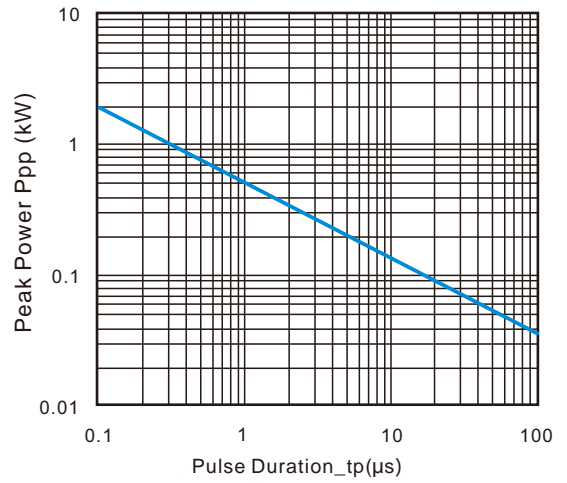


Fig.3 Clamping Voltage vs Peak Pulse Current ($t_p=8/20\mu$ s)

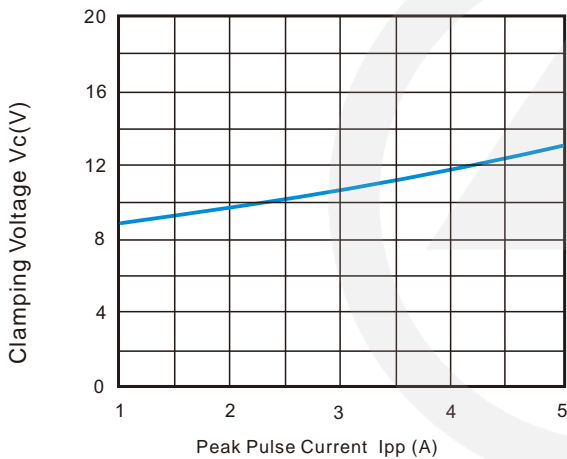


Fig.4 Power Derating Curve

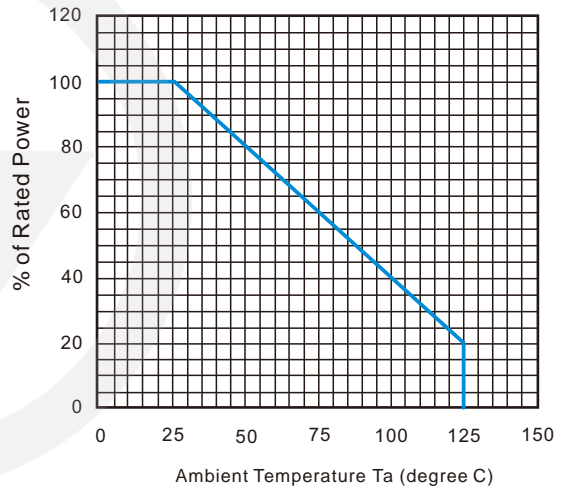


Fig.5 TLP Curve

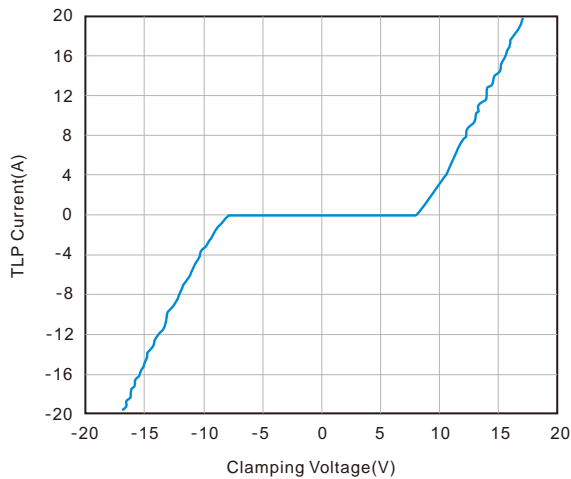
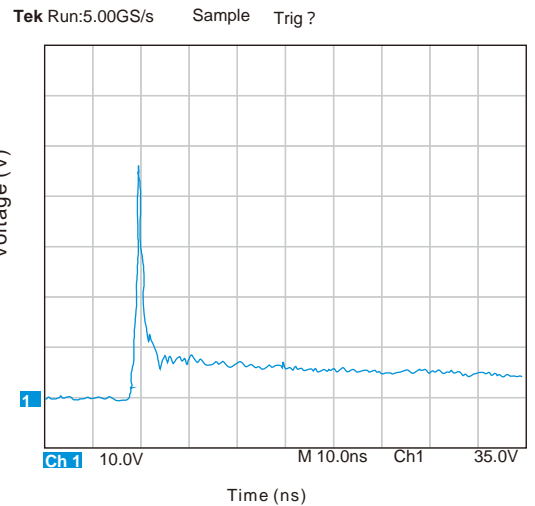
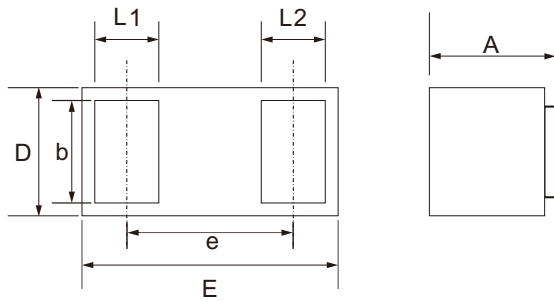


Fig.6 ESD Clamping Voltage 8kV Contact per IEC61000-4-2



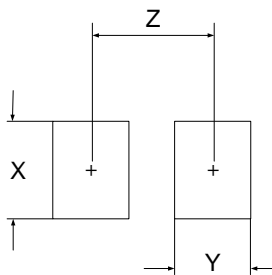
DFN1006 Package Outline

Unit: mm



SYMBOL	DIMENSIONS	
	MIN.	MAX.
D	0.550	0.650
E	0.950	1.050
L1	0.200	0.300
L2	0.200	0.300
b	0.450	0.550
e	0.650 TYP.	
A	0.450	0.550

DFN1006 Suggested Pad Layout



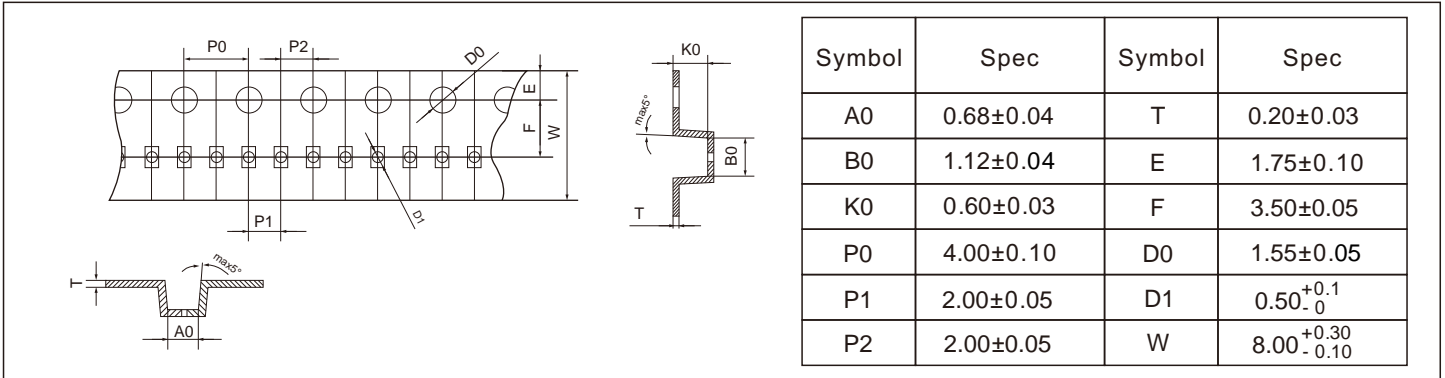
SYM	DIMENSIONS
	MILLIMETERS
X	0.50
Y	0.50
Z	0.90

Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference purpose only.

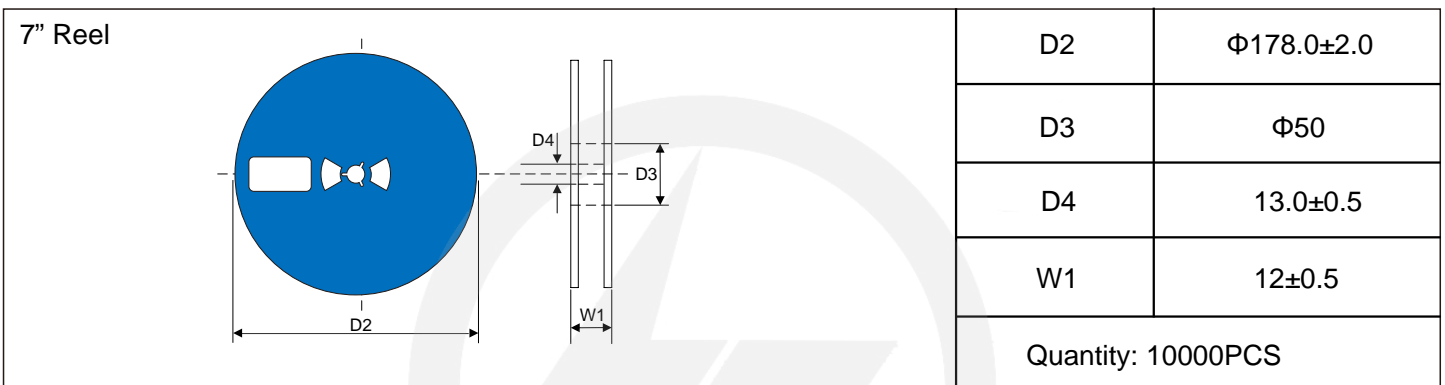
Carrier Tape Dimensions

Unit : mm

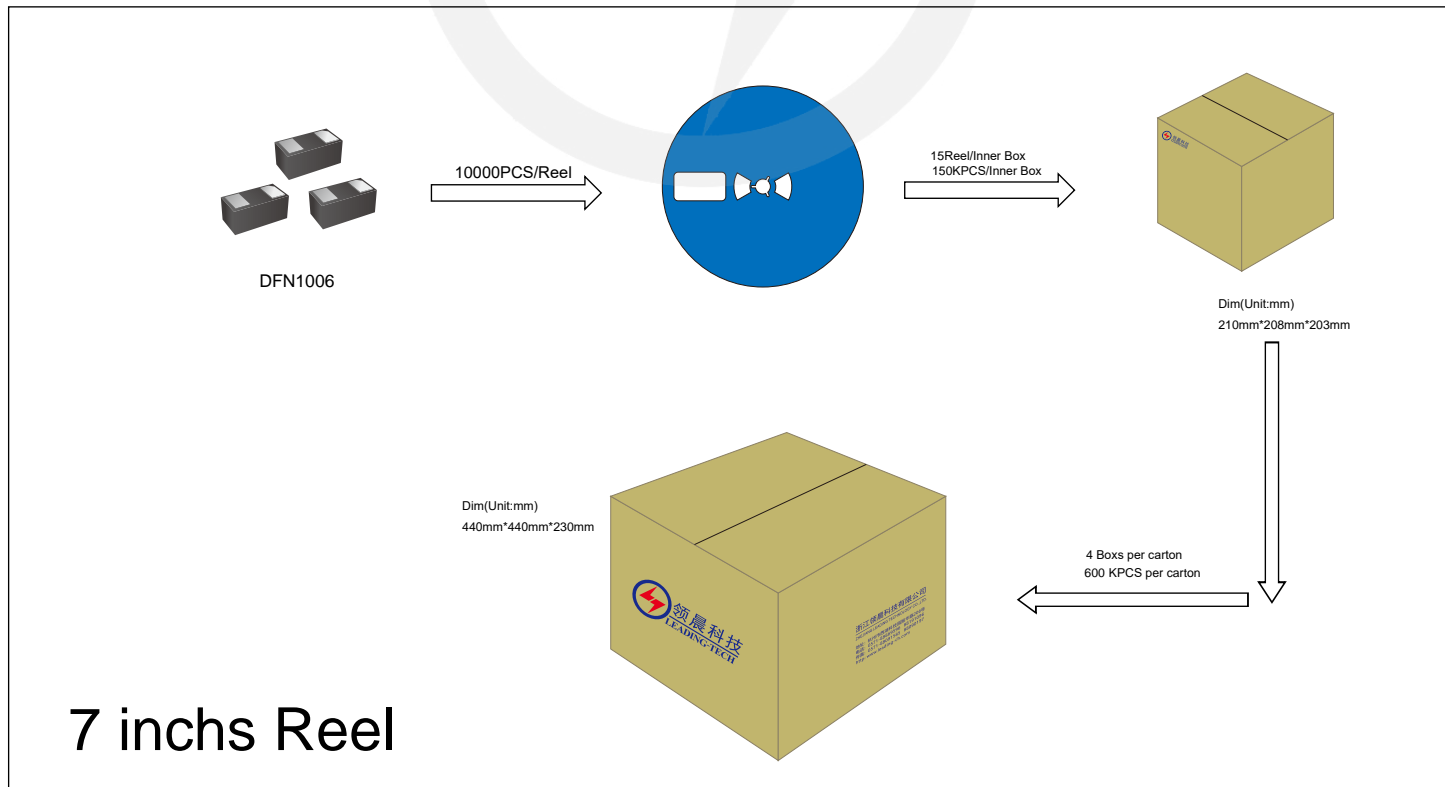


Reel Dimensions

Unit : mm



Packaging





Recommended Soldering Conditions



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T _L to T _P)	3°C/second max.
Preheat	
-Temperature Min (T _{S min})	150°C
-Temperature Max (T _{S max})	200°C
-Time (min to max) (t _s)	60-180 seconds
T _{S max} to T _L	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature (T _L)	217°C
-Time (t _L)	60-150 seconds
Peak Temperature (T _P)	260°C
Time within 5°C of actual Peak Temperature (t _p)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

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Version Update Information

Series NO.	Enactment/Revision Date	Effective Date	Version	Revision content	Revision Reason	Revision Person	Note
01	2024.09.10	2024.09.10	3.0	New File	/	Ding	